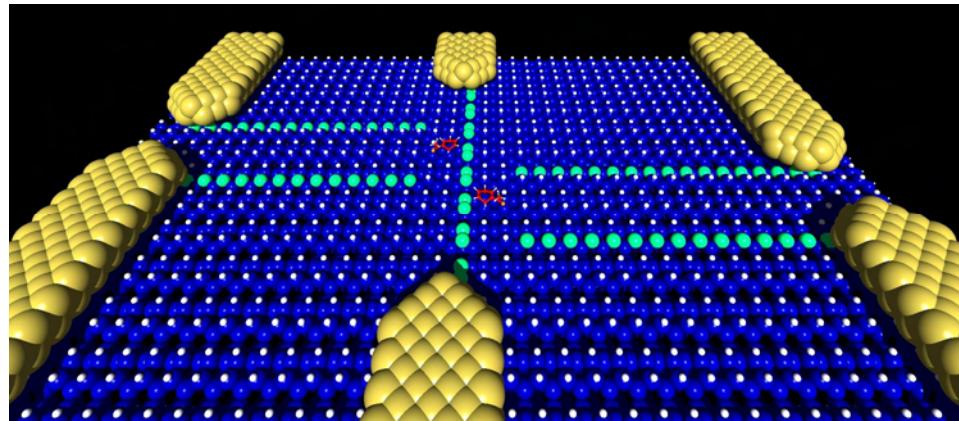


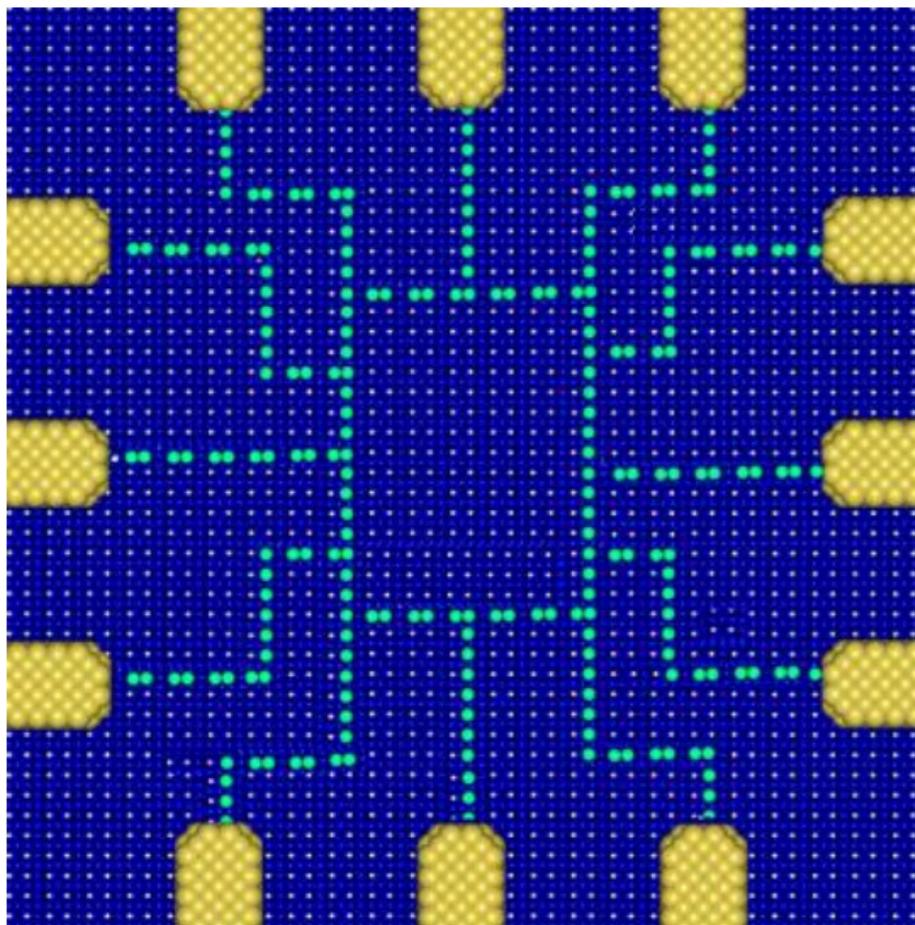
Large dangling bond electronic circuits with the supporting surface and contacting nano-pads

Francisco Ample, Hiroyo Kawai, Mark Saeys,
Kian Soon Yong, Kuan Eng Johnson Goh



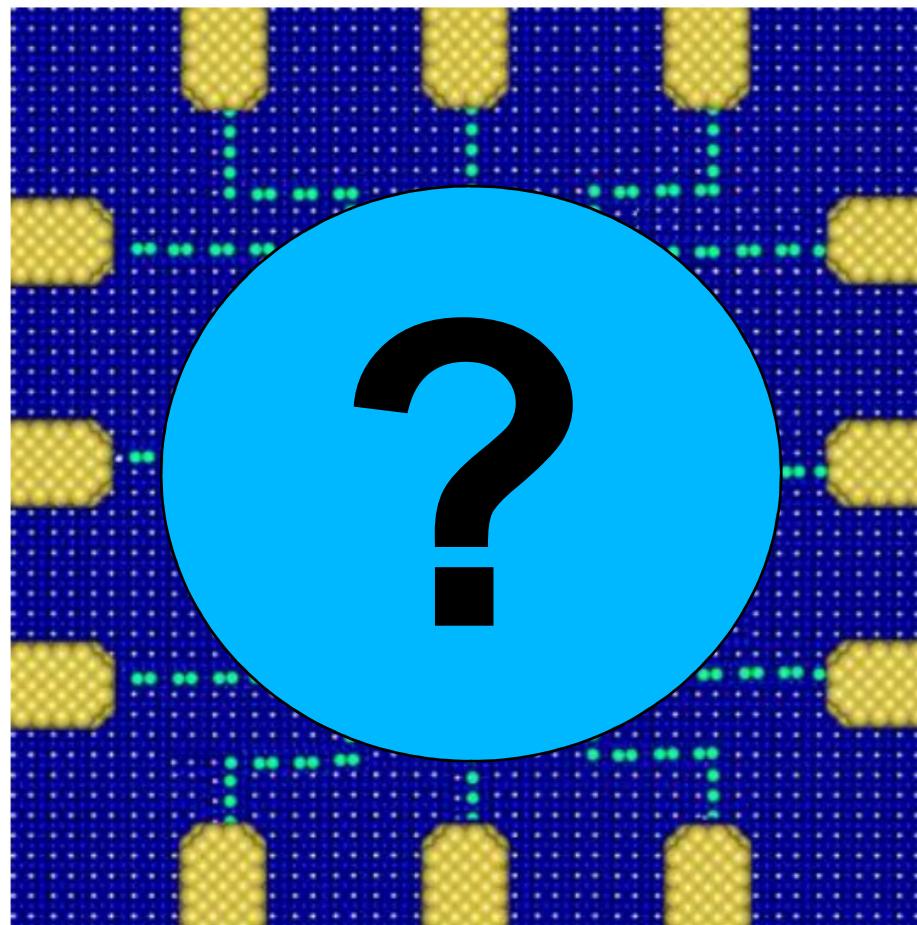
OBJECTIVE

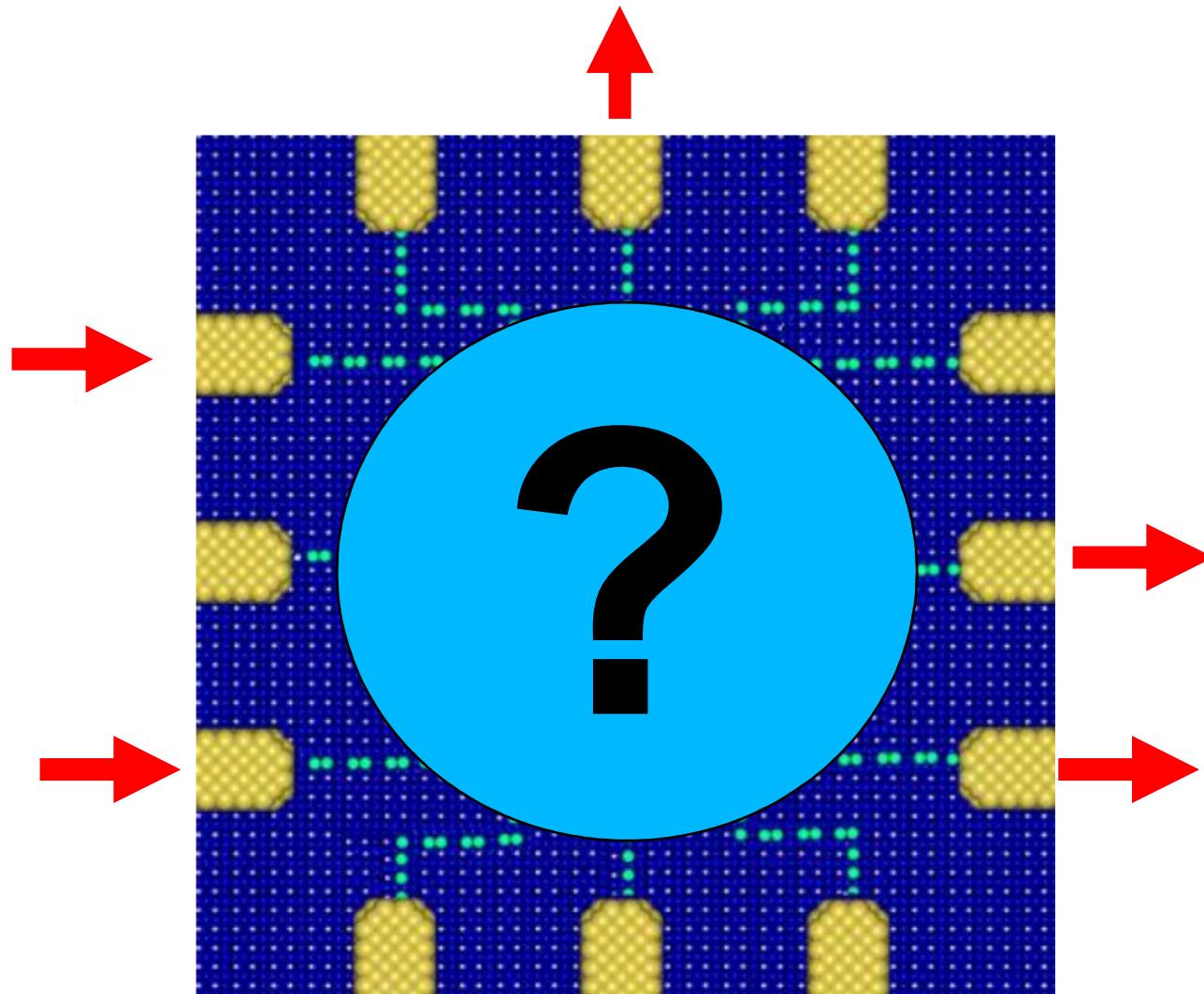
-theoretical design of atomic scale circuits



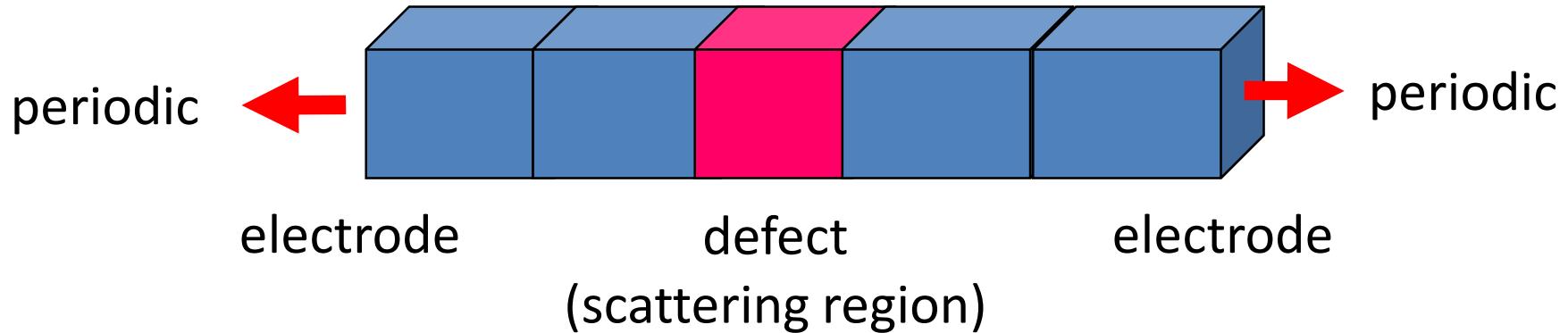
OBJECTIVE

-theoretical design of atomic scale circuits





An example of a multi-channel scattering problem



$$\text{Schrodinger Equation}$$
$$H\psi = E\psi$$

Spatial propagator

Transfer Matrix

Spatial propagator Kernel:
Green function

=

Scattering Matrix

ESQC (Effective
Hamiltonian)

Scattering Matrix

Self-Energy

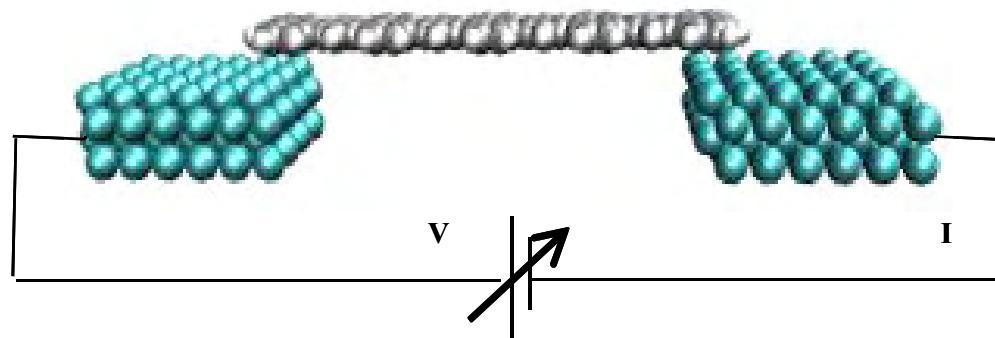
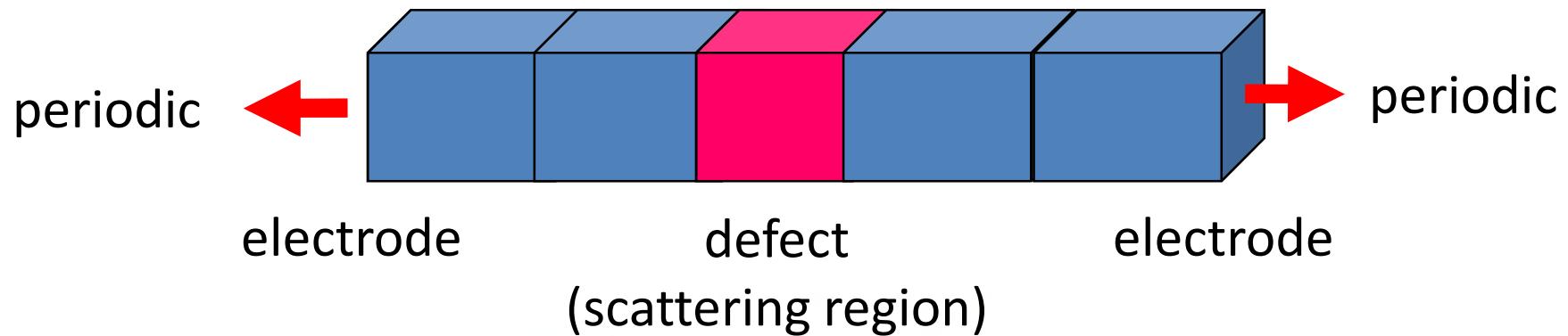


Amplitudes of the incoming
and outgoing plane waves

$$T(E) = \frac{|C|^2}{|A|^2}$$

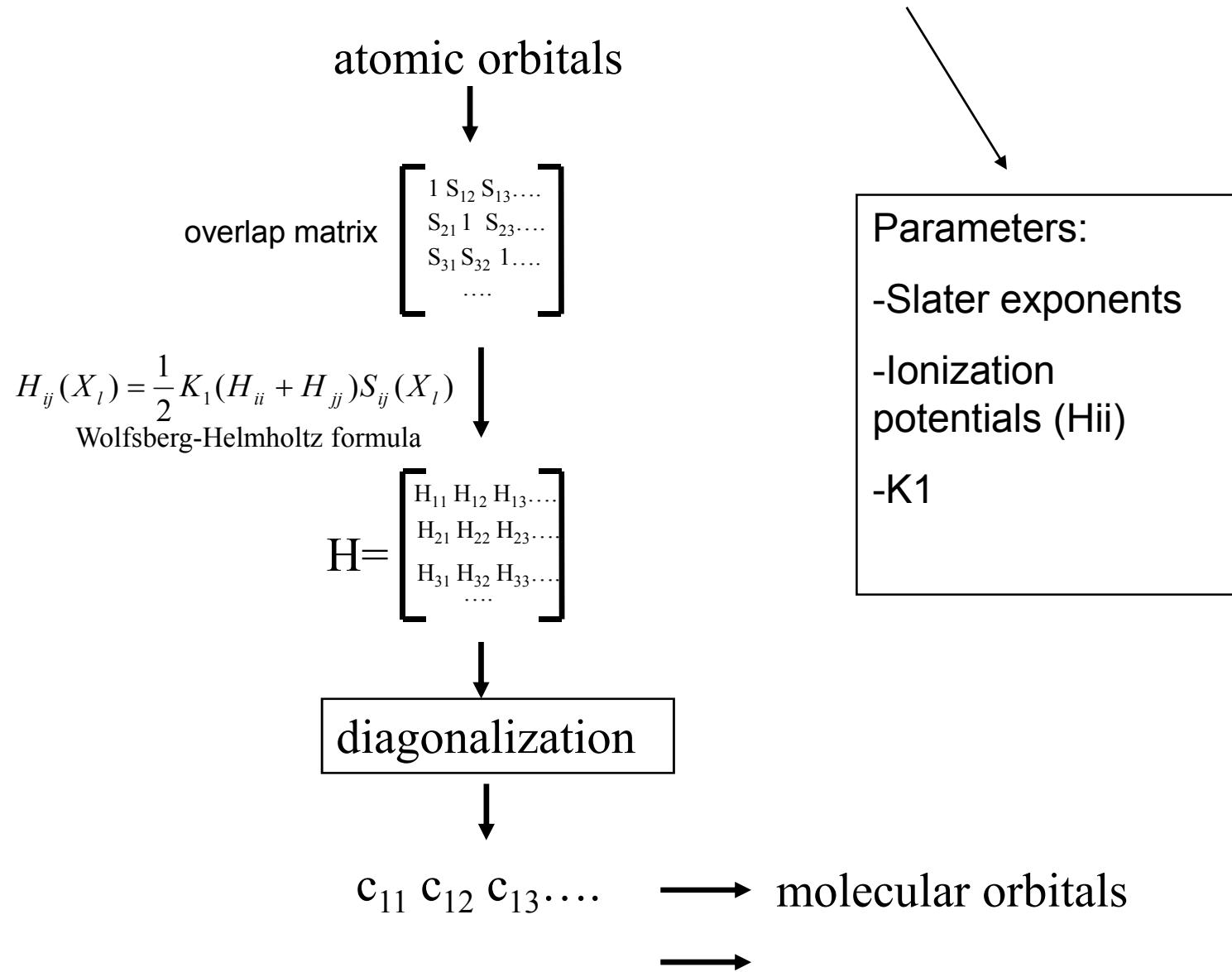
$$I(V, x, y, z) = \frac{e}{\pi \hbar} \int_{E_f}^{E_f + eV} T(E, x, y, z) dE$$

ESQC



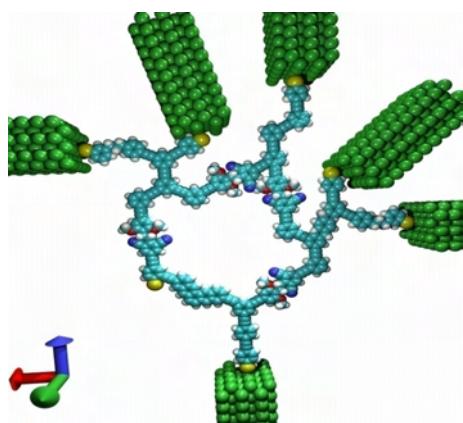
Extended Huckel MO approximation:
-parameters are fitted with comparison with DFT calculations

LCAO Basis set + DFT fitting

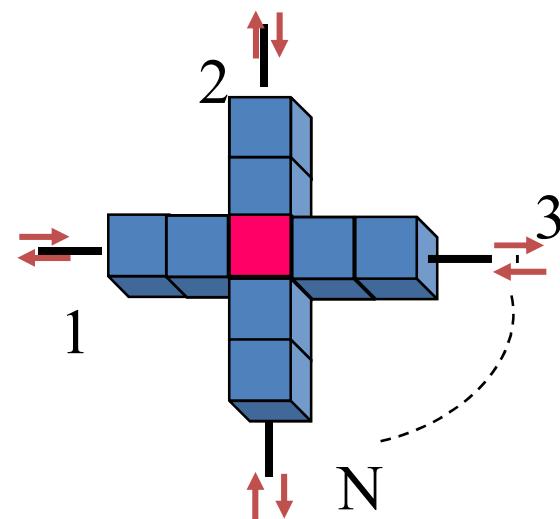


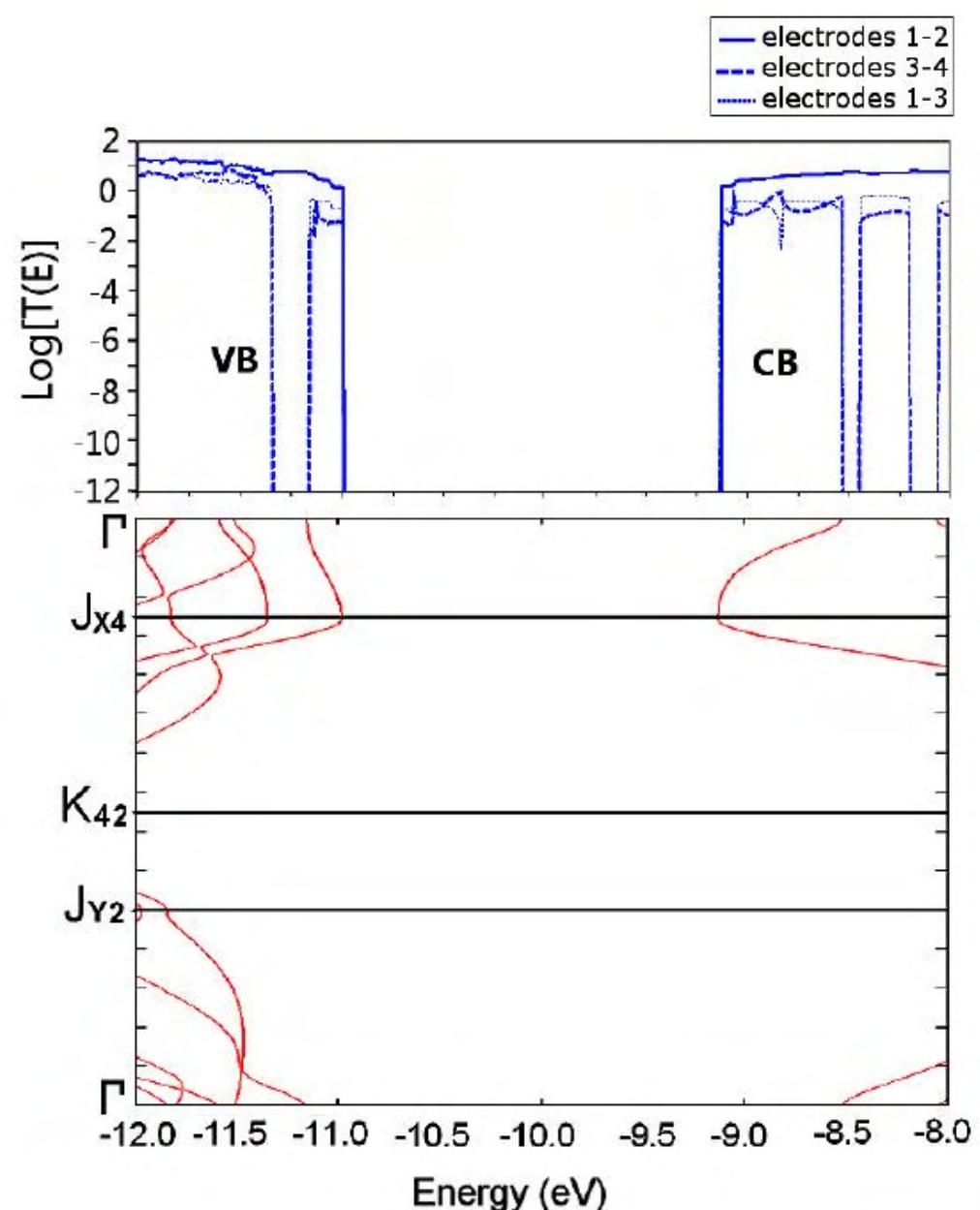
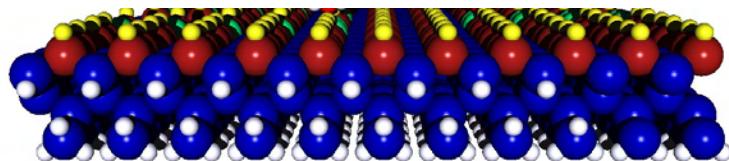
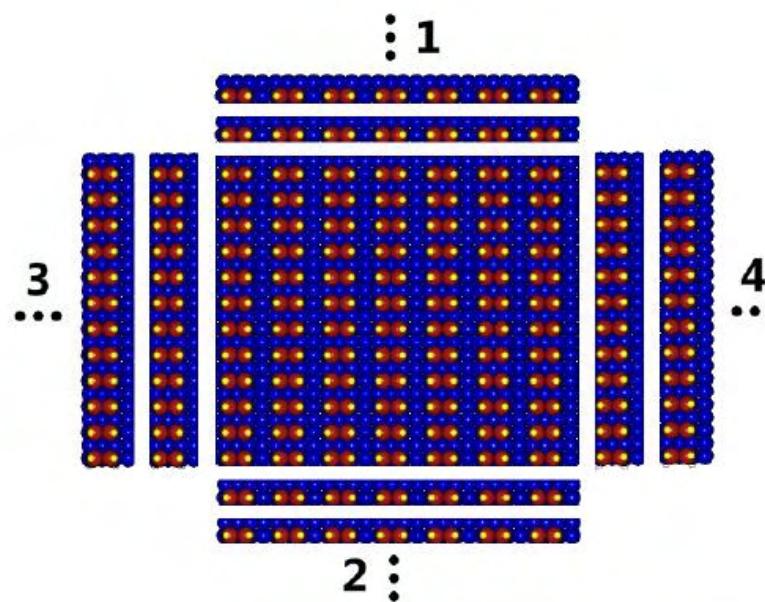
N-ESQC

- electron transport calculations of atomic scale circuits driven in a ballistic or tunneling regime + the supporting surface
- Elastic Scattering Quantum Chemistry technique
- full scattering matrix for N electrodes

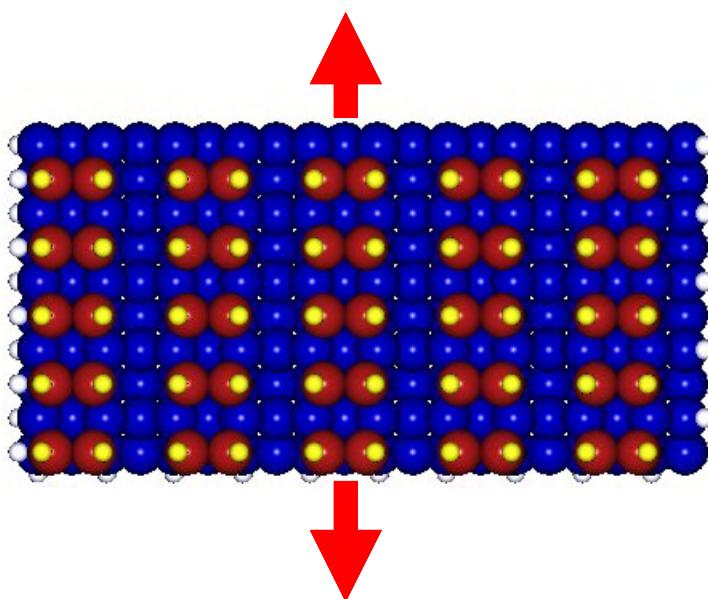


S. Ami and C. Joachim, Phys. Rev. B 65 (2002) 155419

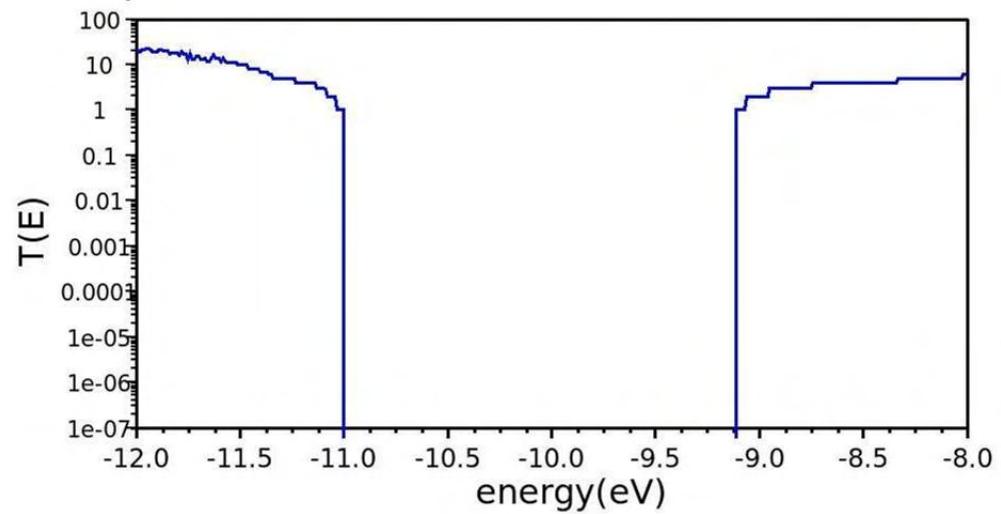
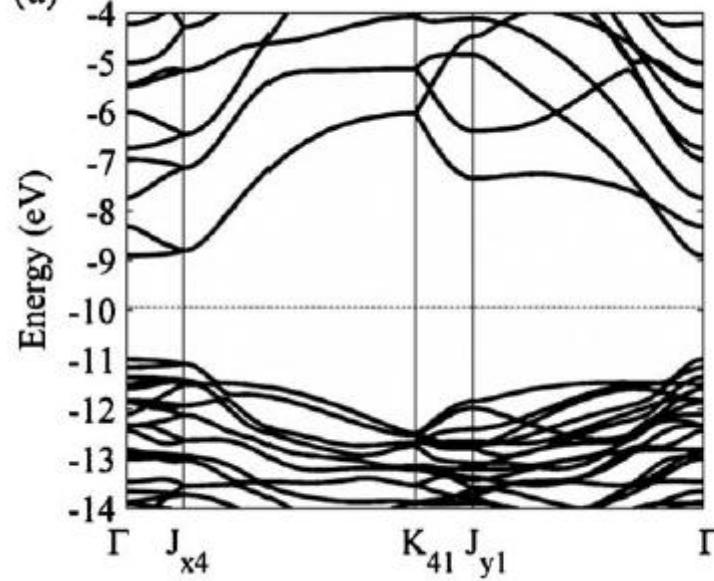




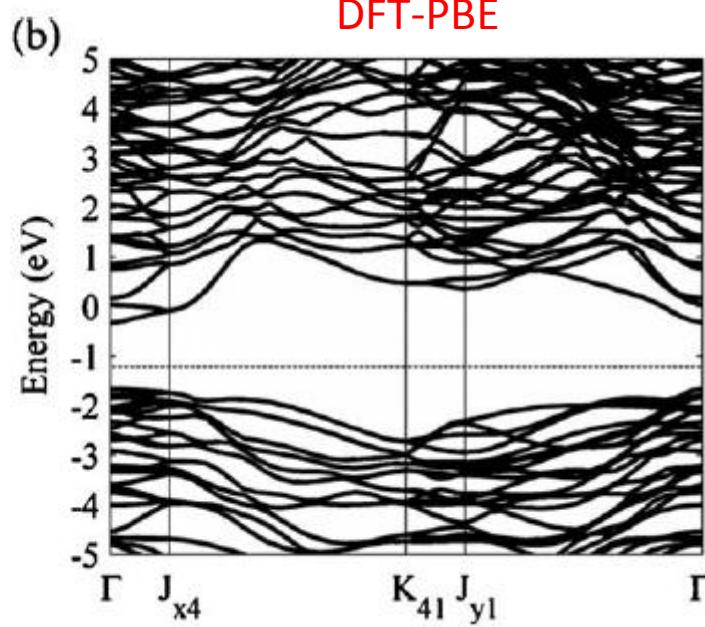
Si(001)H



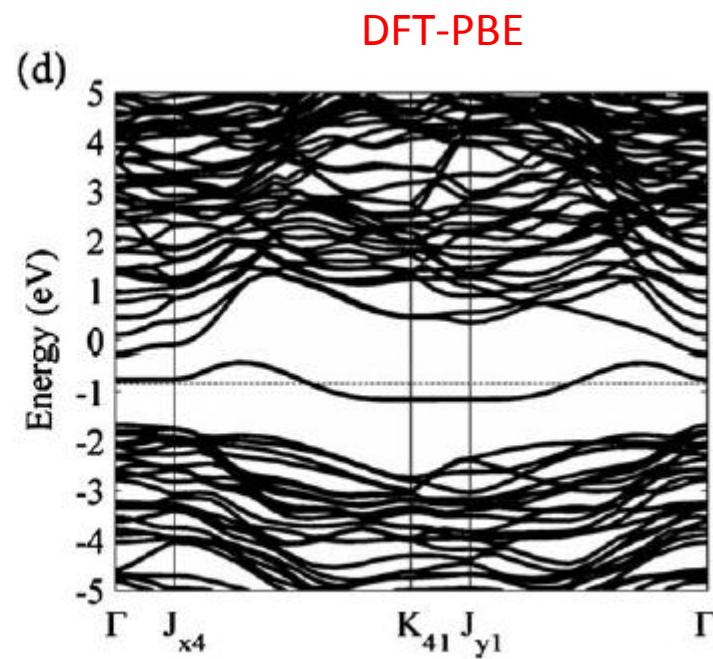
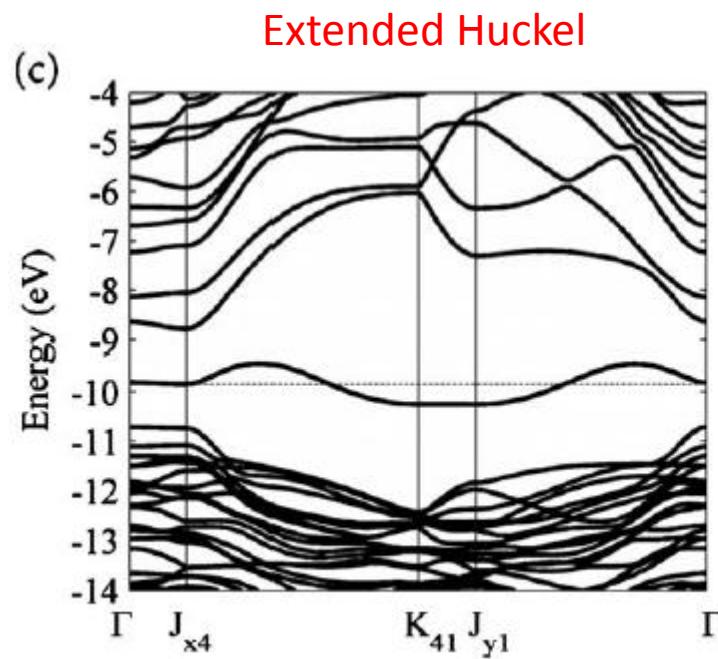
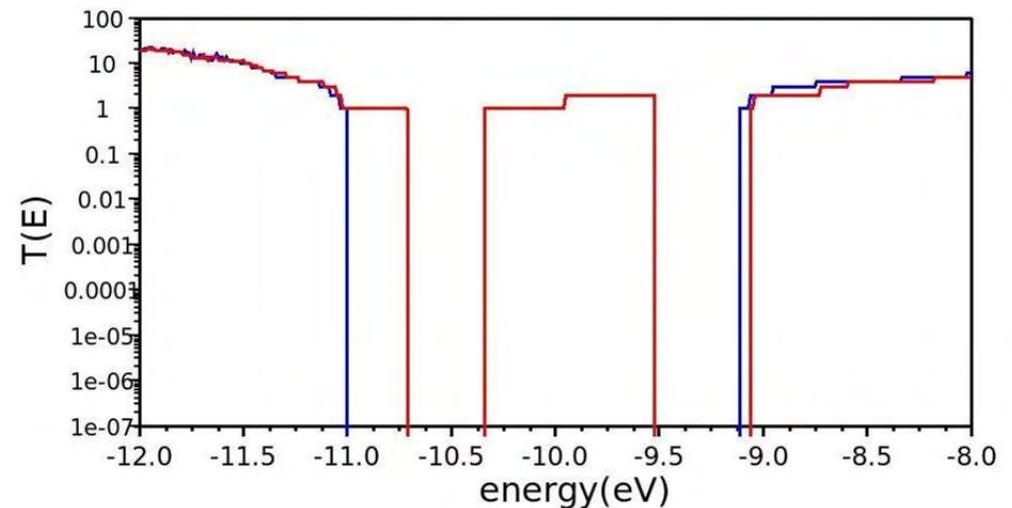
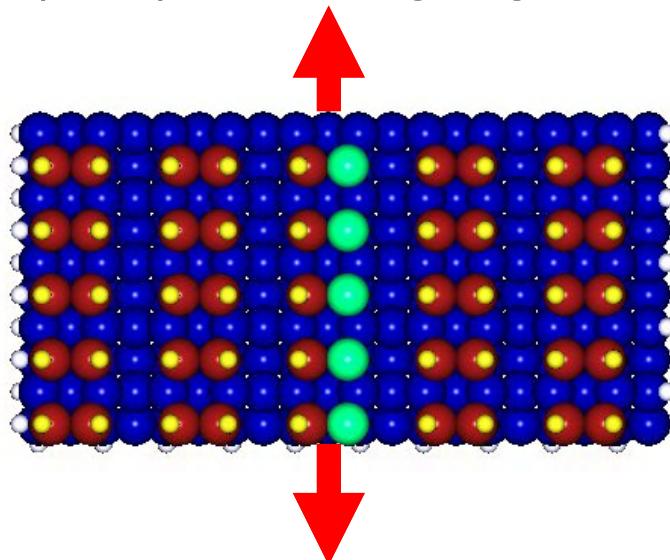
(a) Extended Huckel

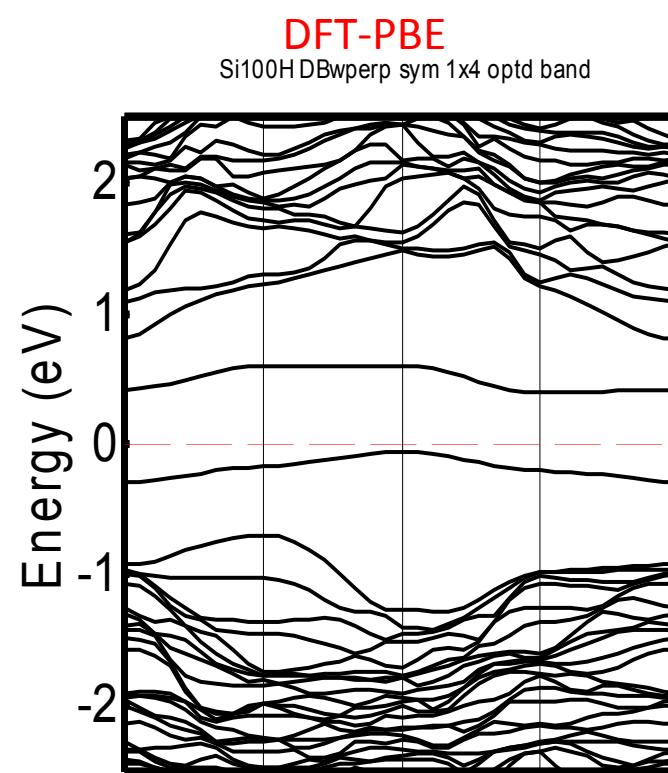
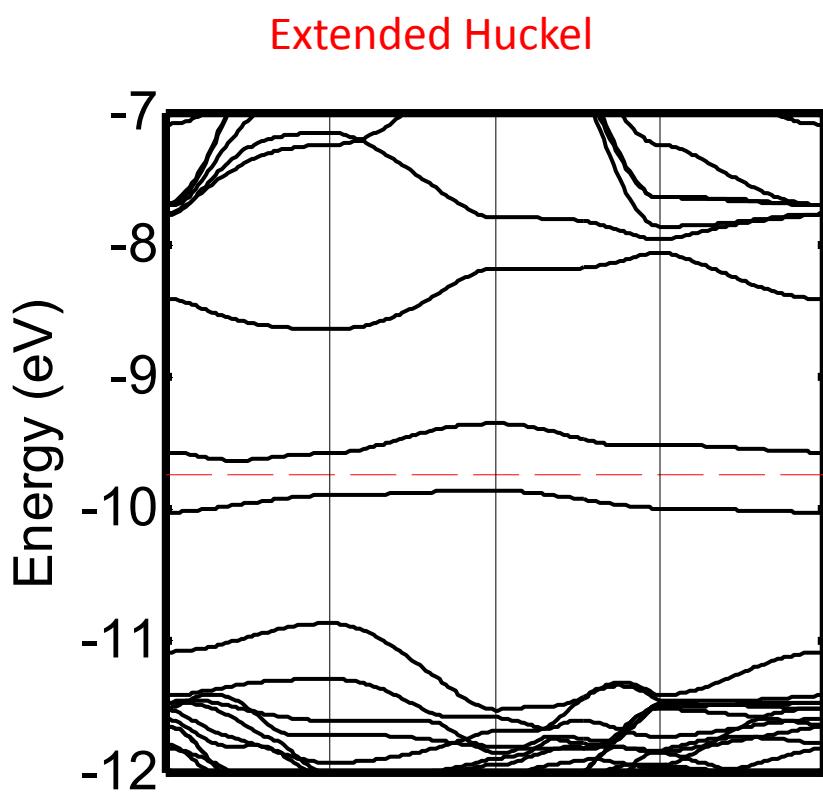
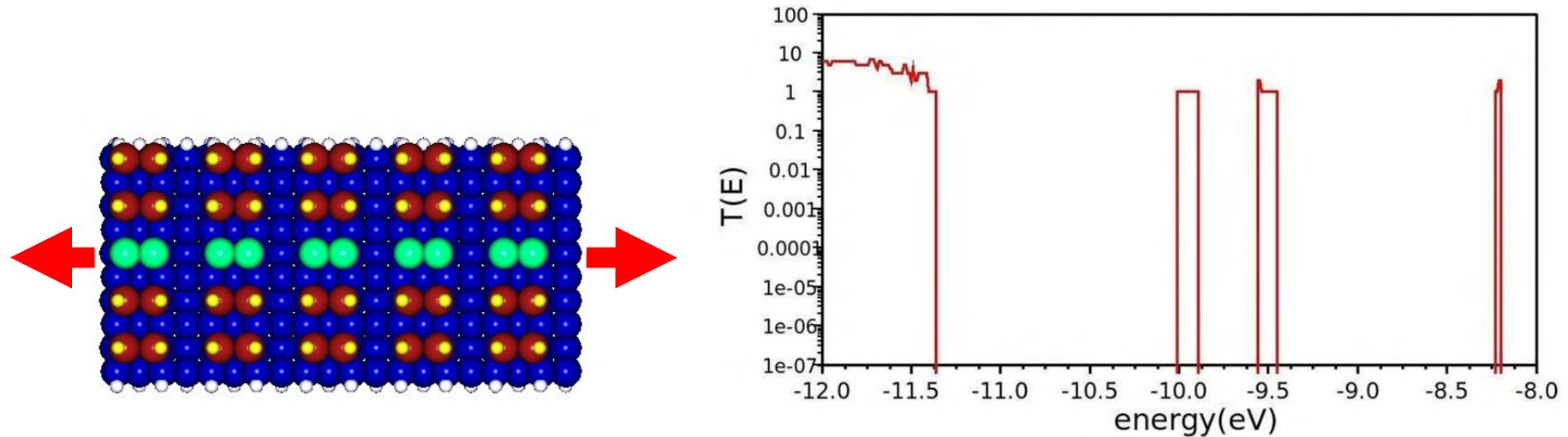


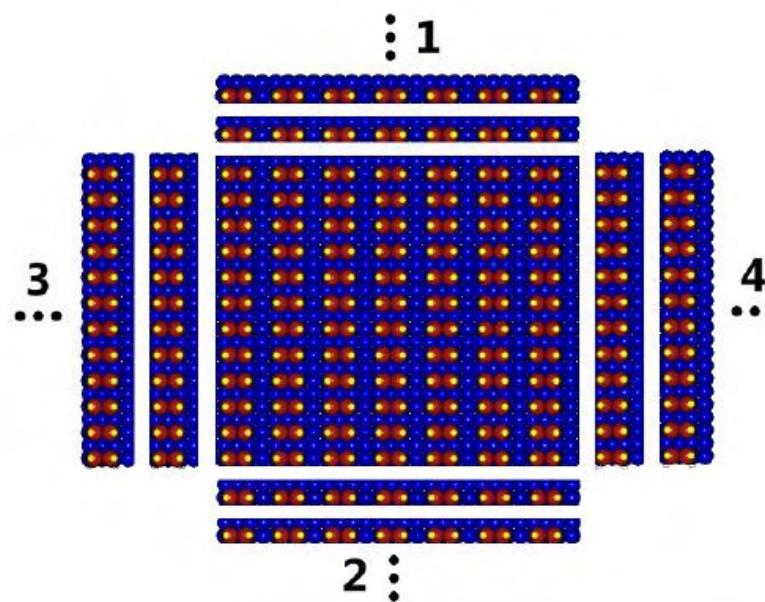
DFT-PBE



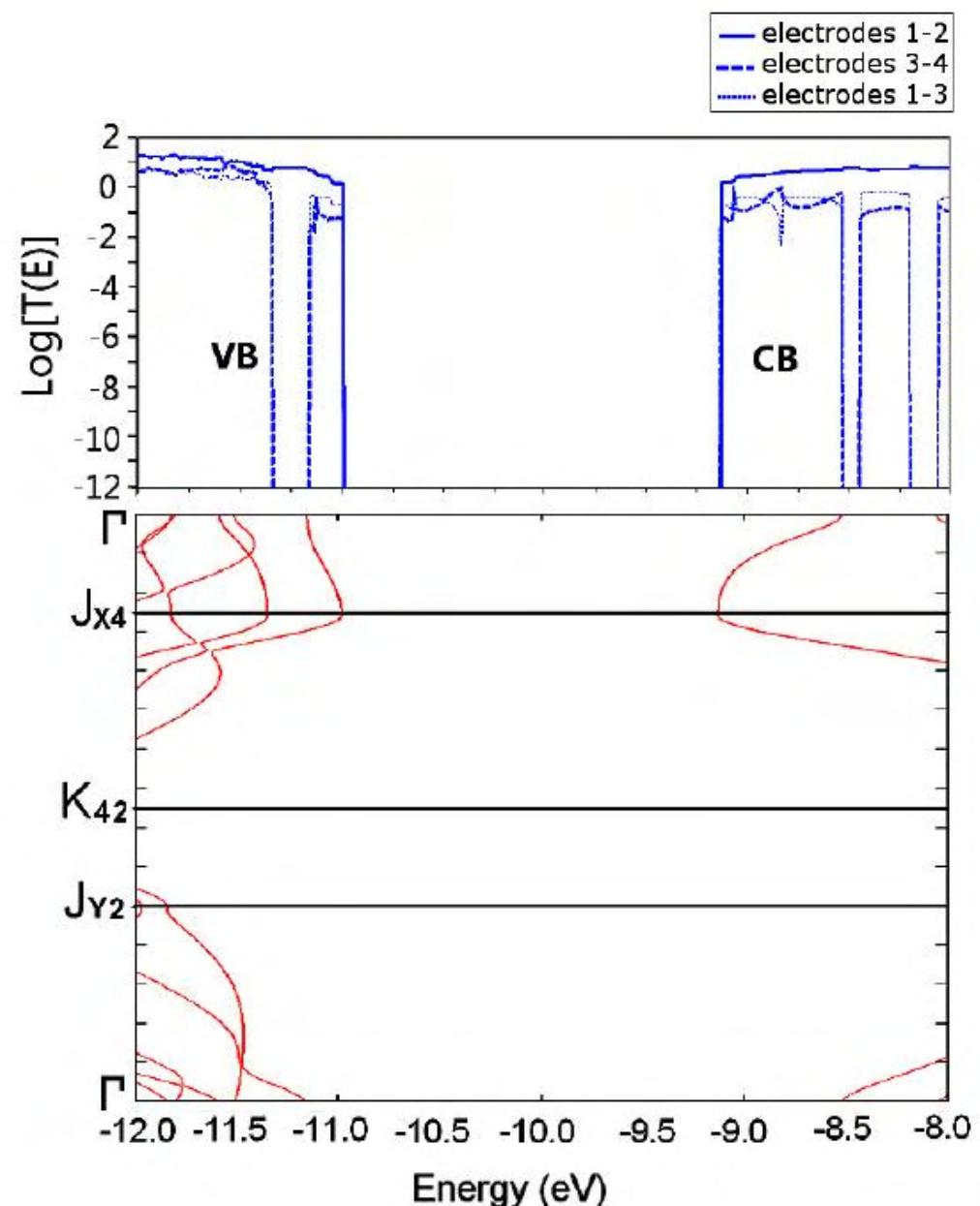
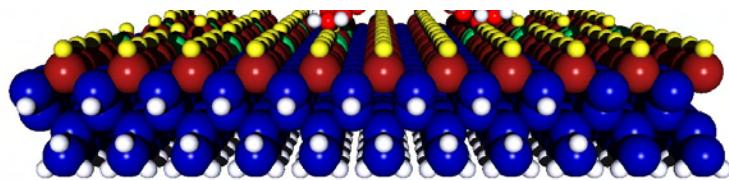
Si(001)H + dangling wire

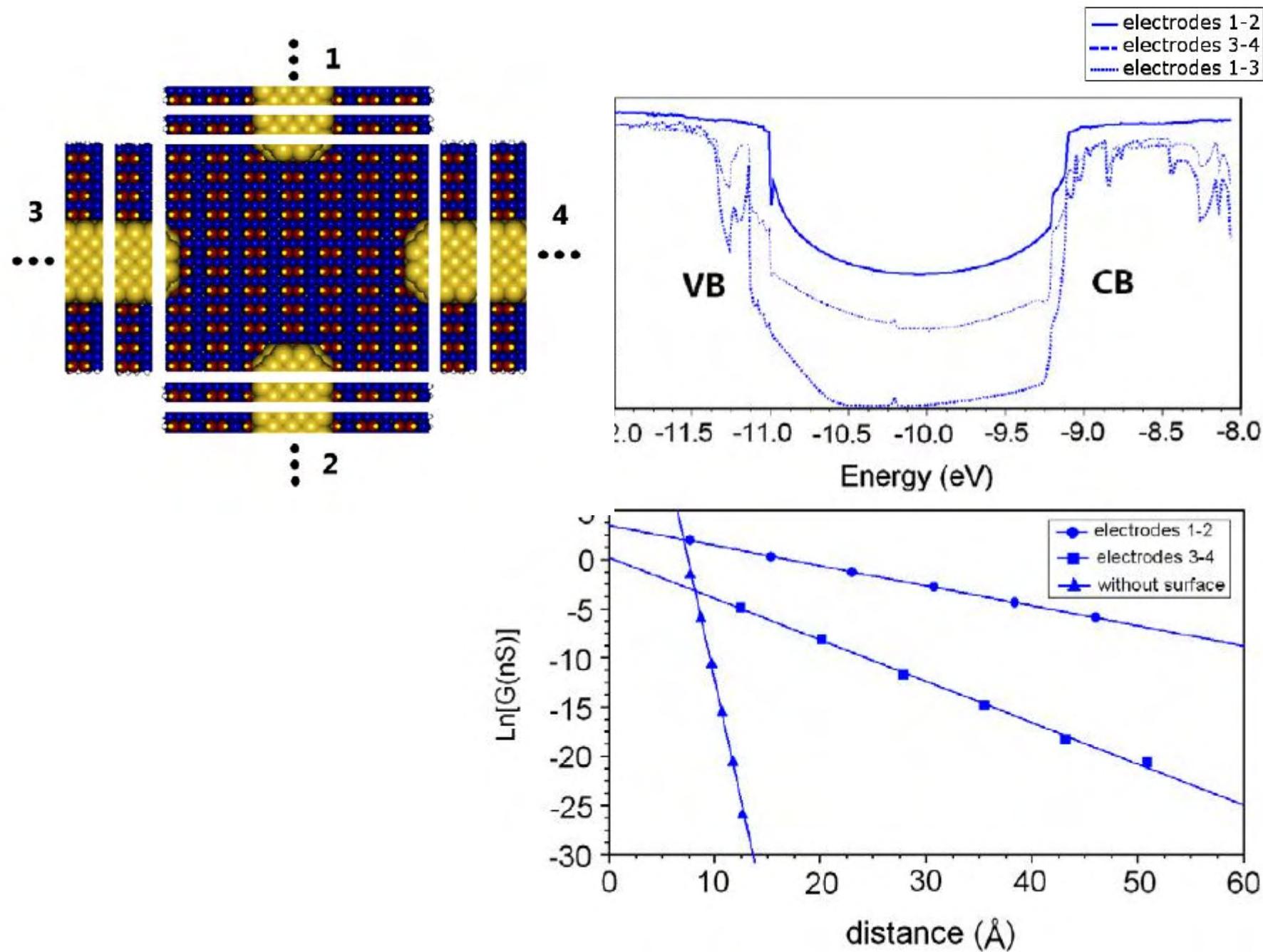




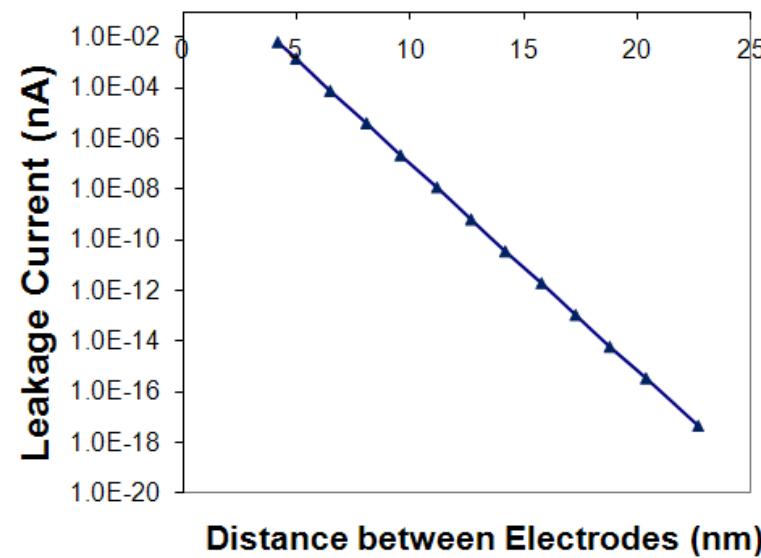
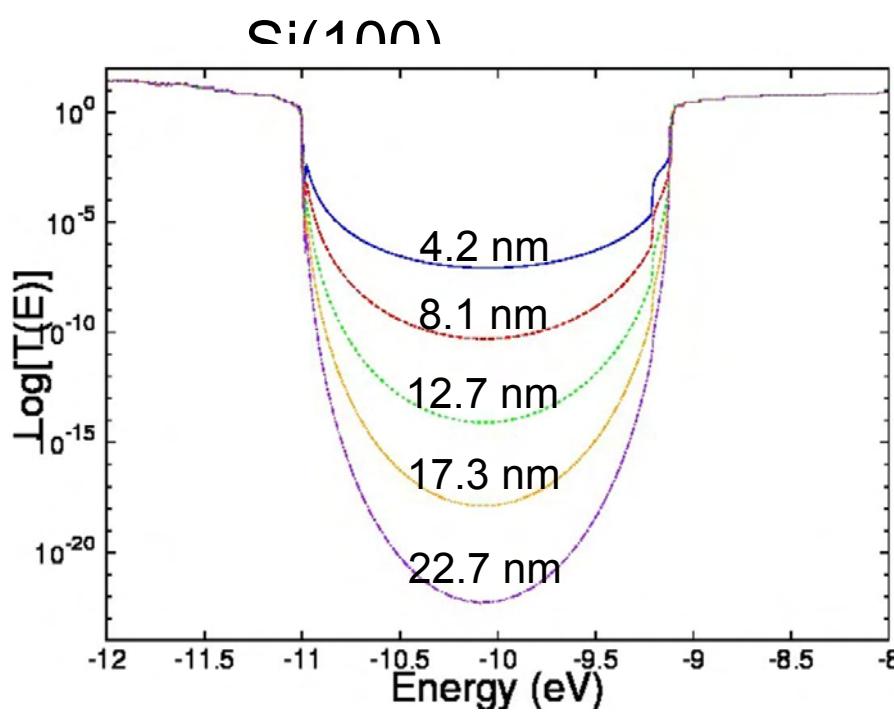
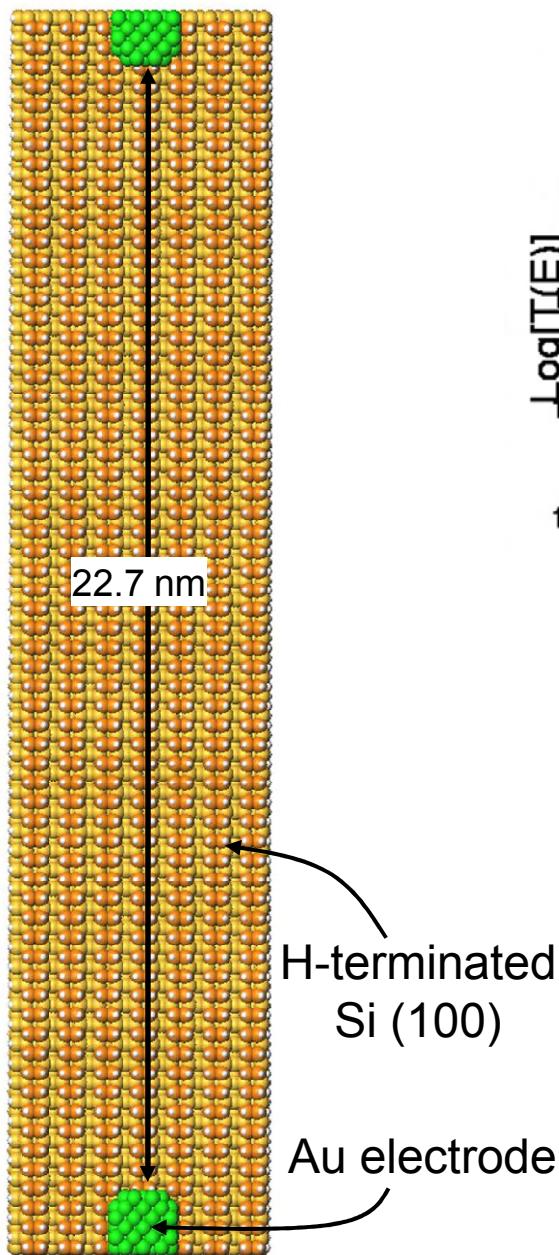


3696 atoms



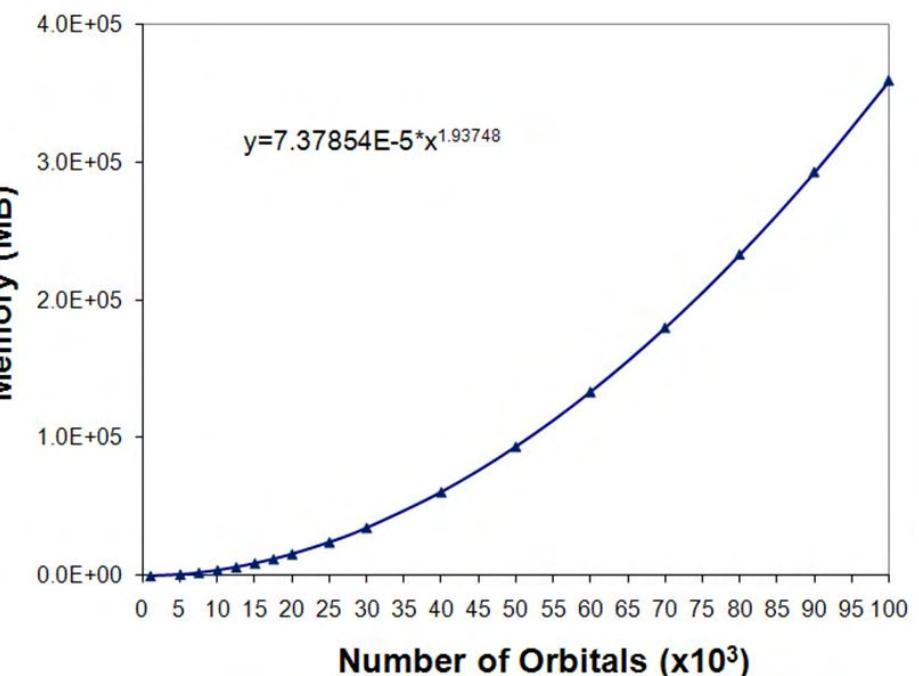
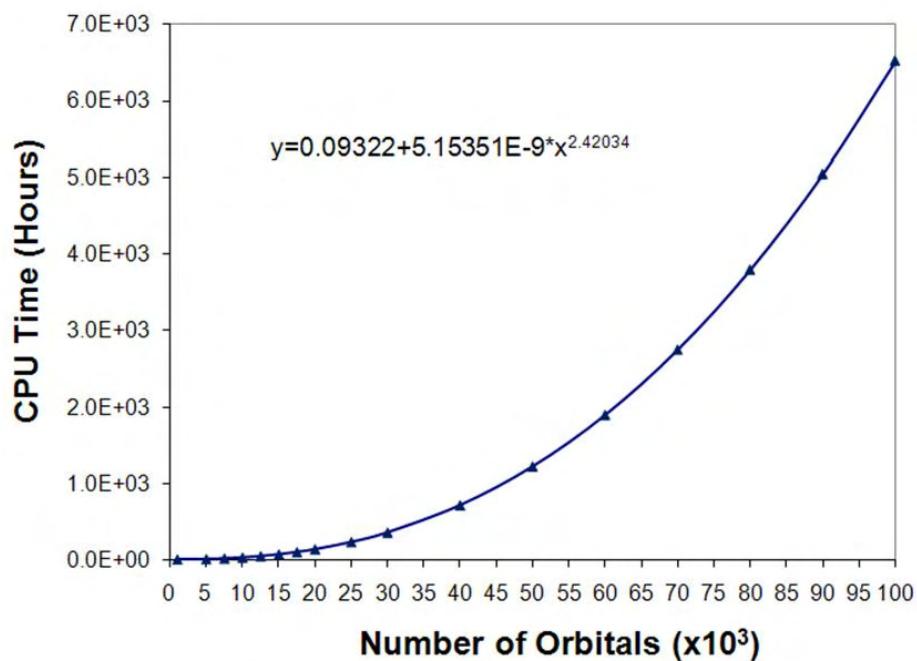
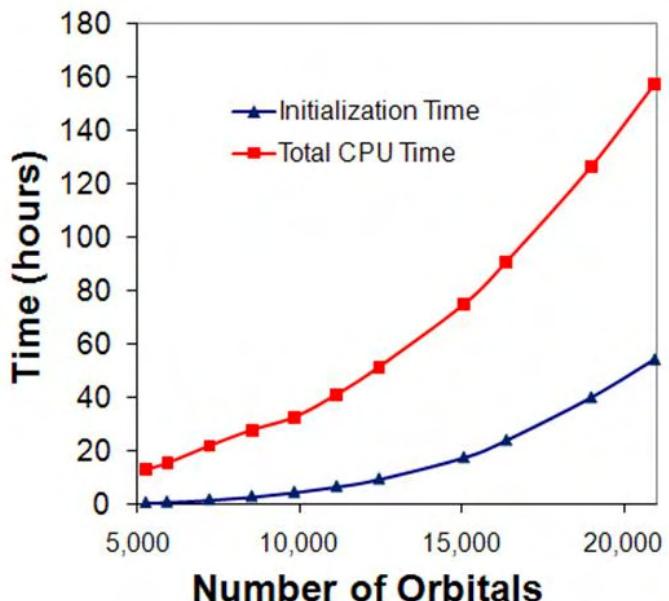


$T(E)$ and leakage current with different inter-electrode distance on Si(100)



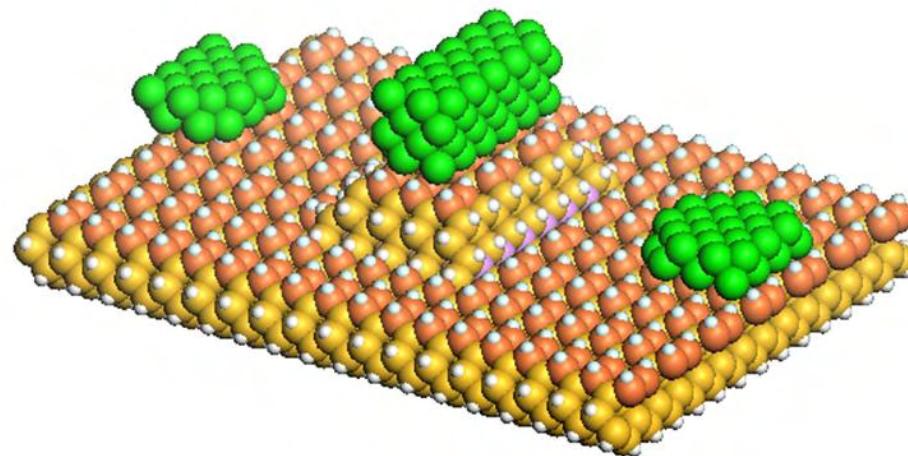
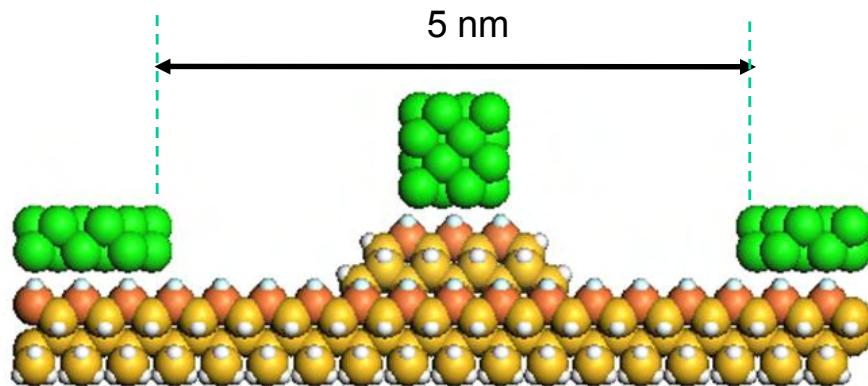
Fitted curves for CPU time and memory vs orbital number

Inter-Au Distance (nm)	Orbitals #
4.2	5289
8.1	8549
12.7	12461
17.3	16373
22.7	20937

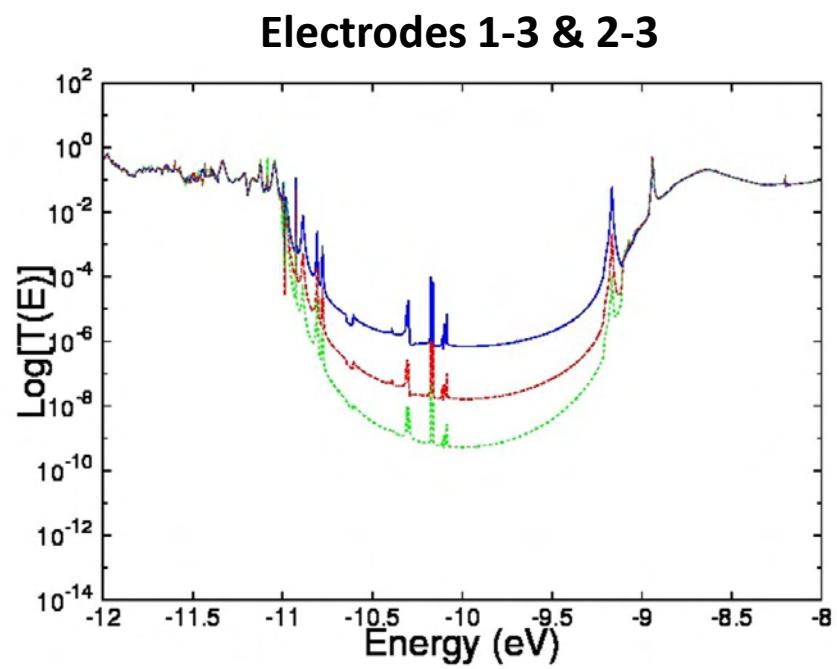
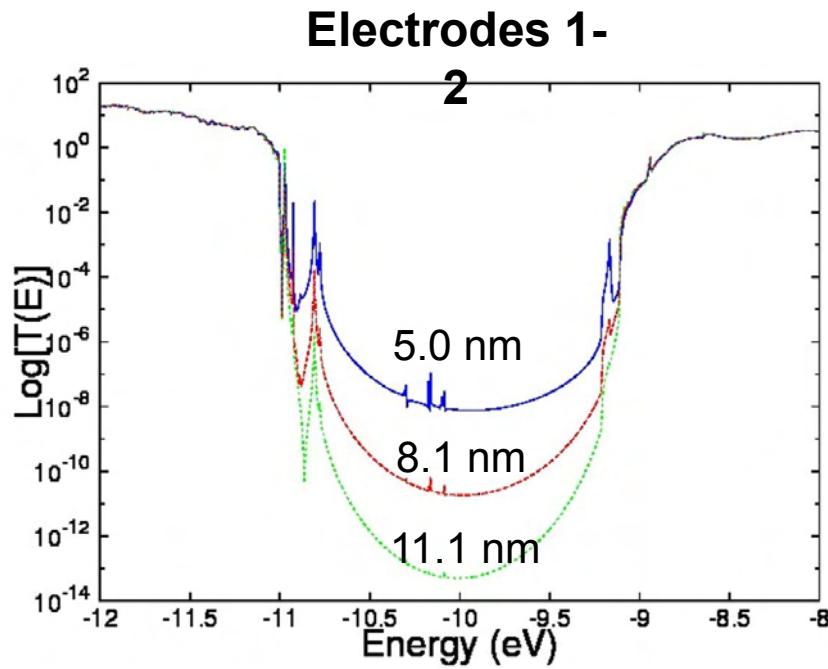
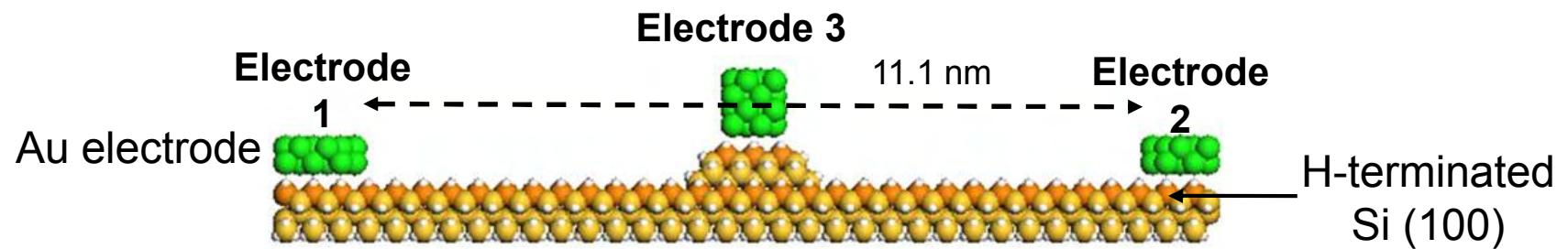


Scalling down the transistor down to the atomic scale ?

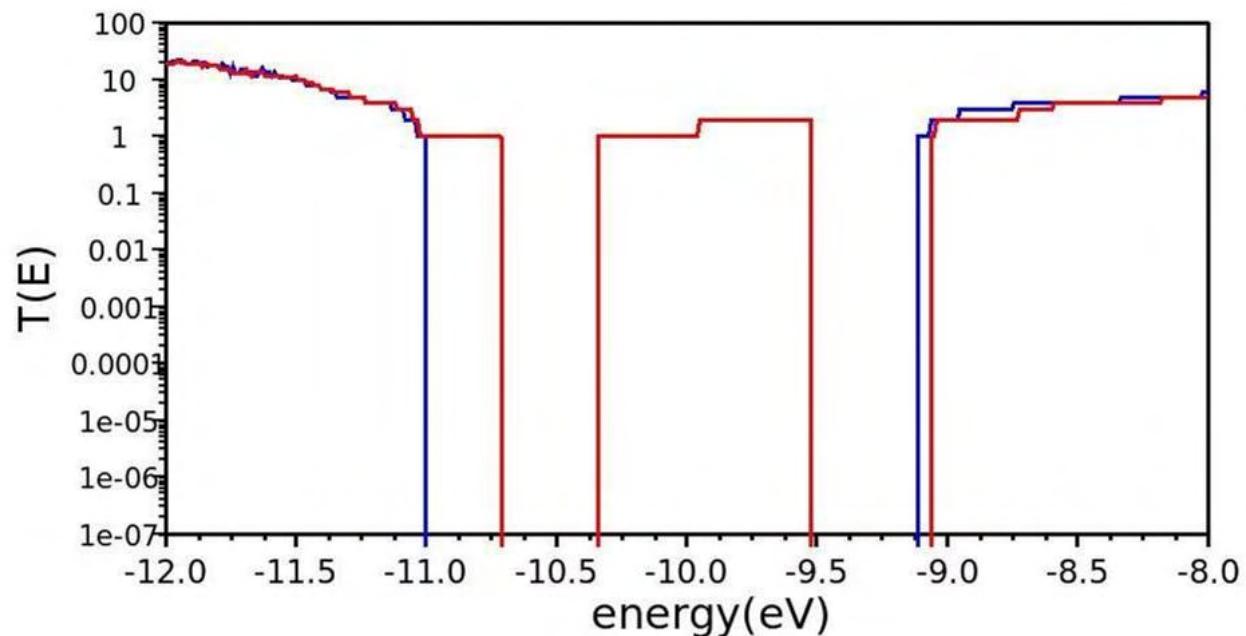
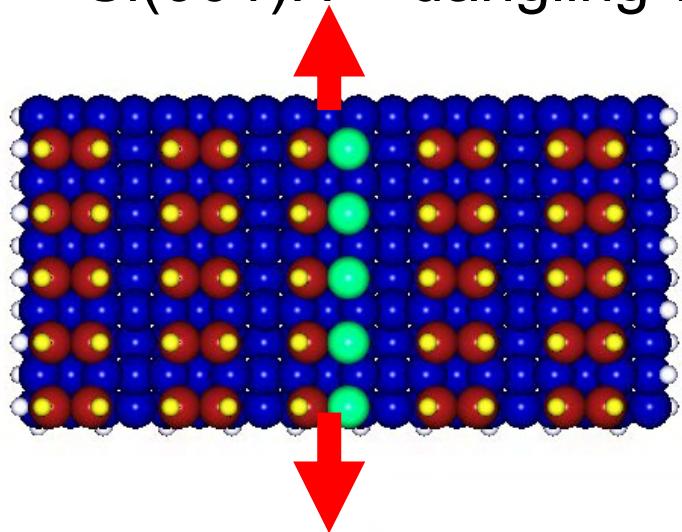
3-terminals device to emulate a transistor

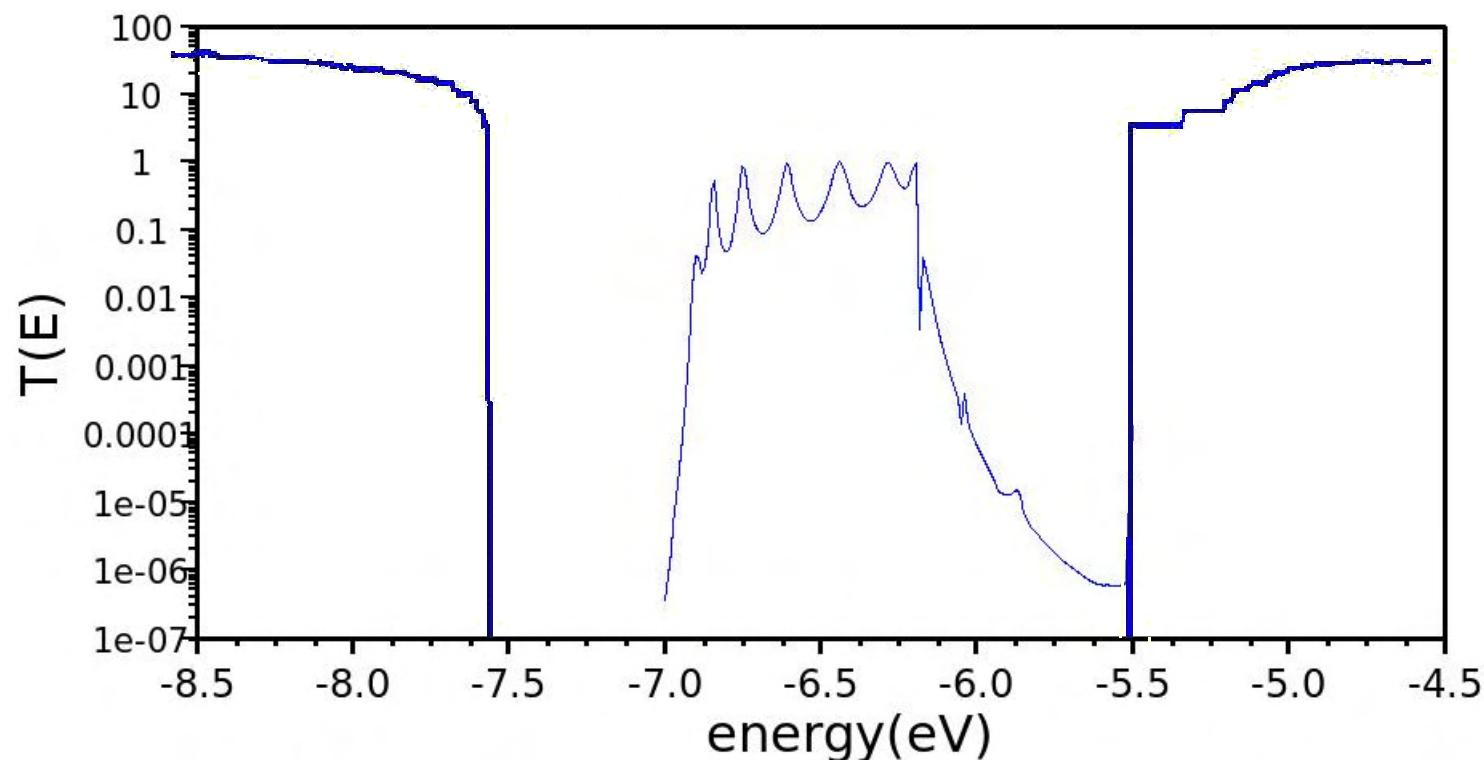
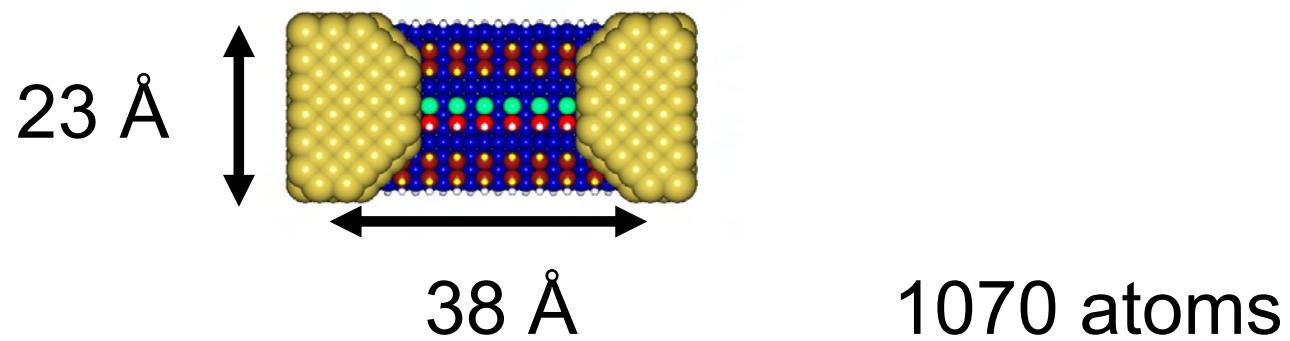


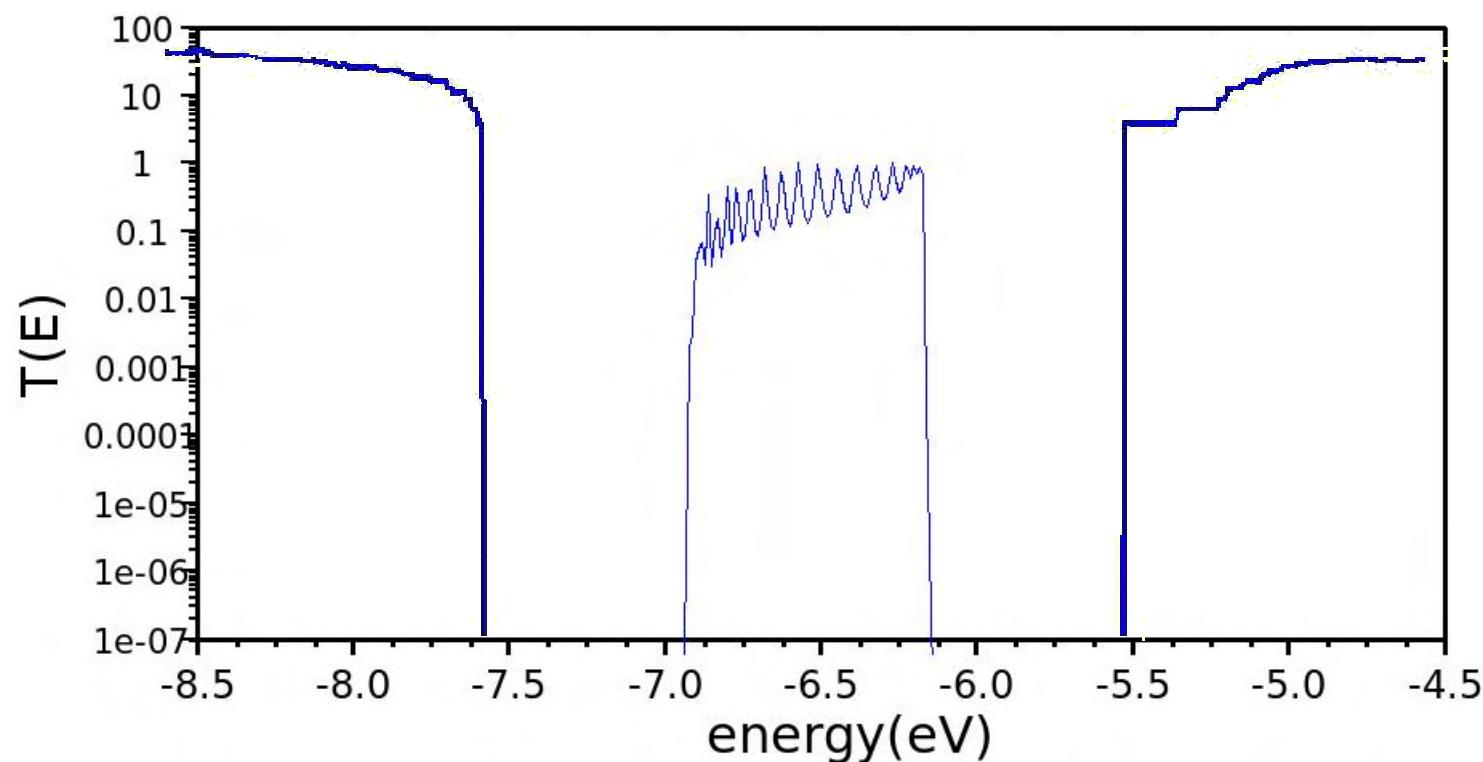
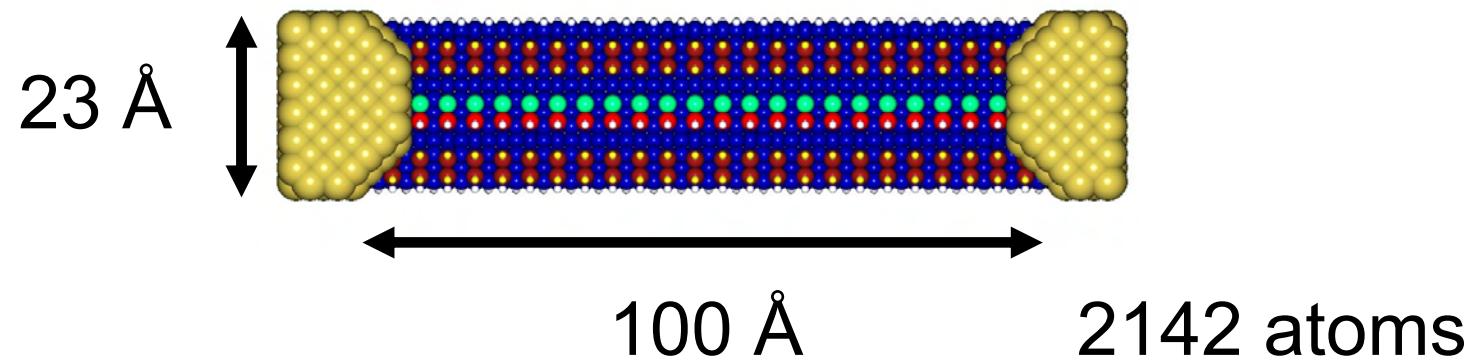
3-terminals structures that resemble transistors

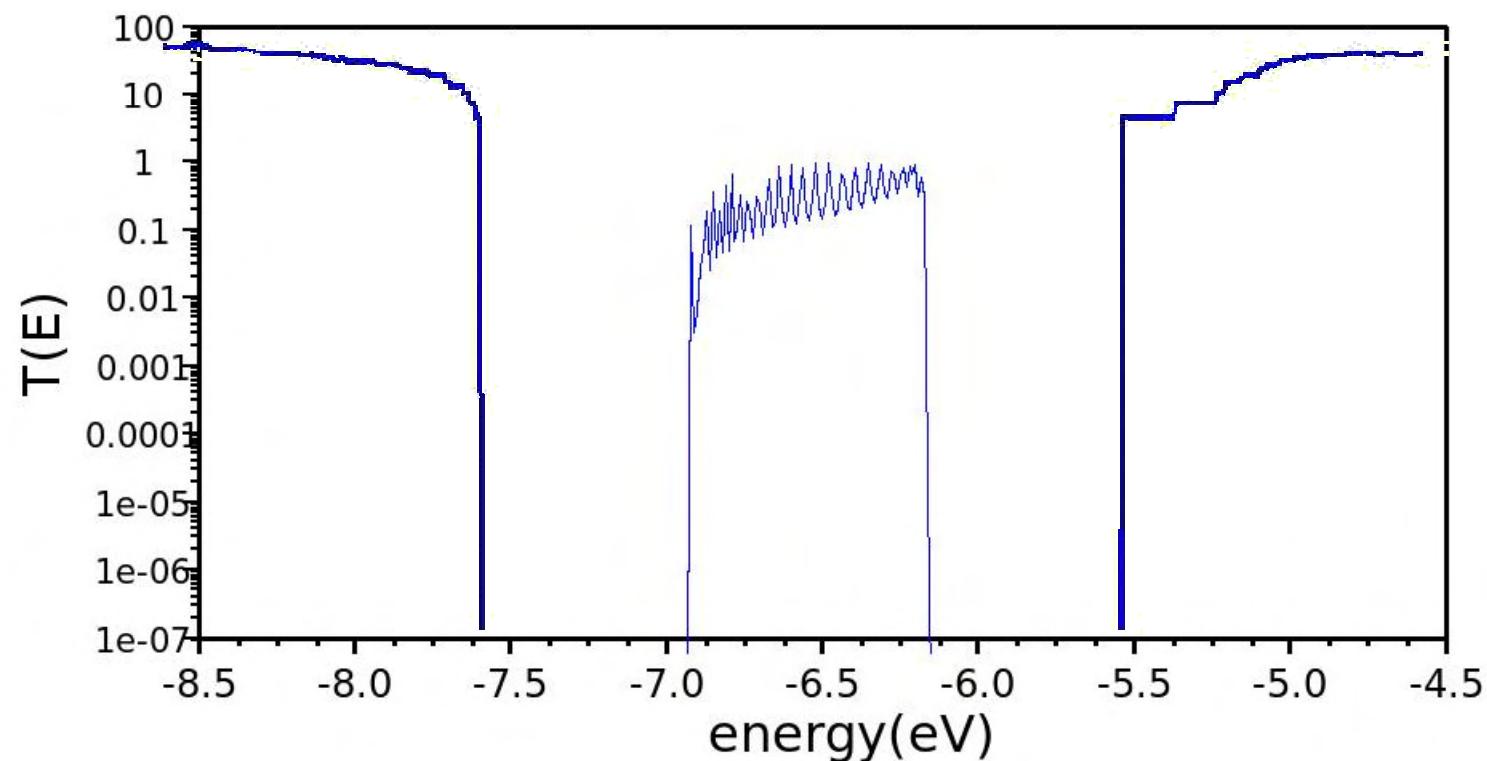
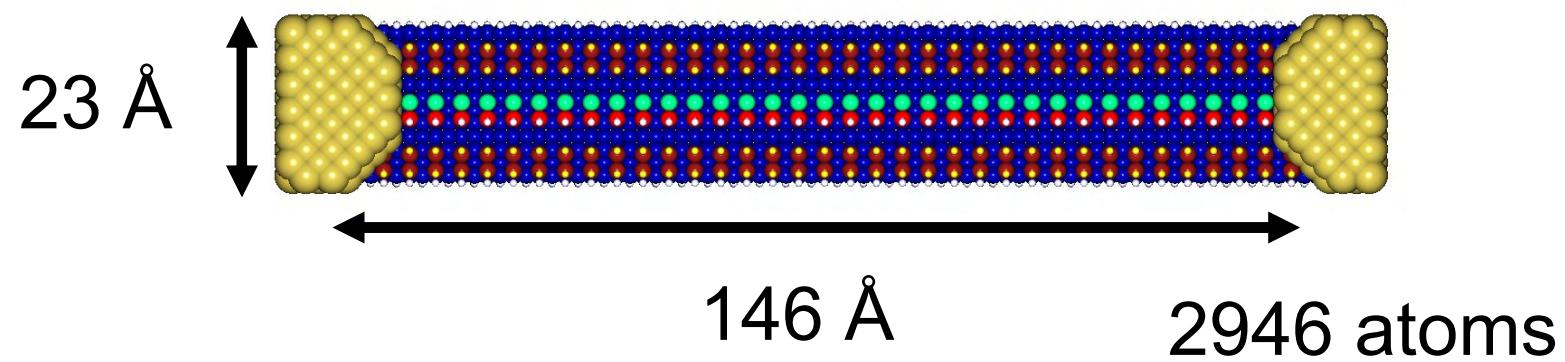


Si(001)H + dangling wire

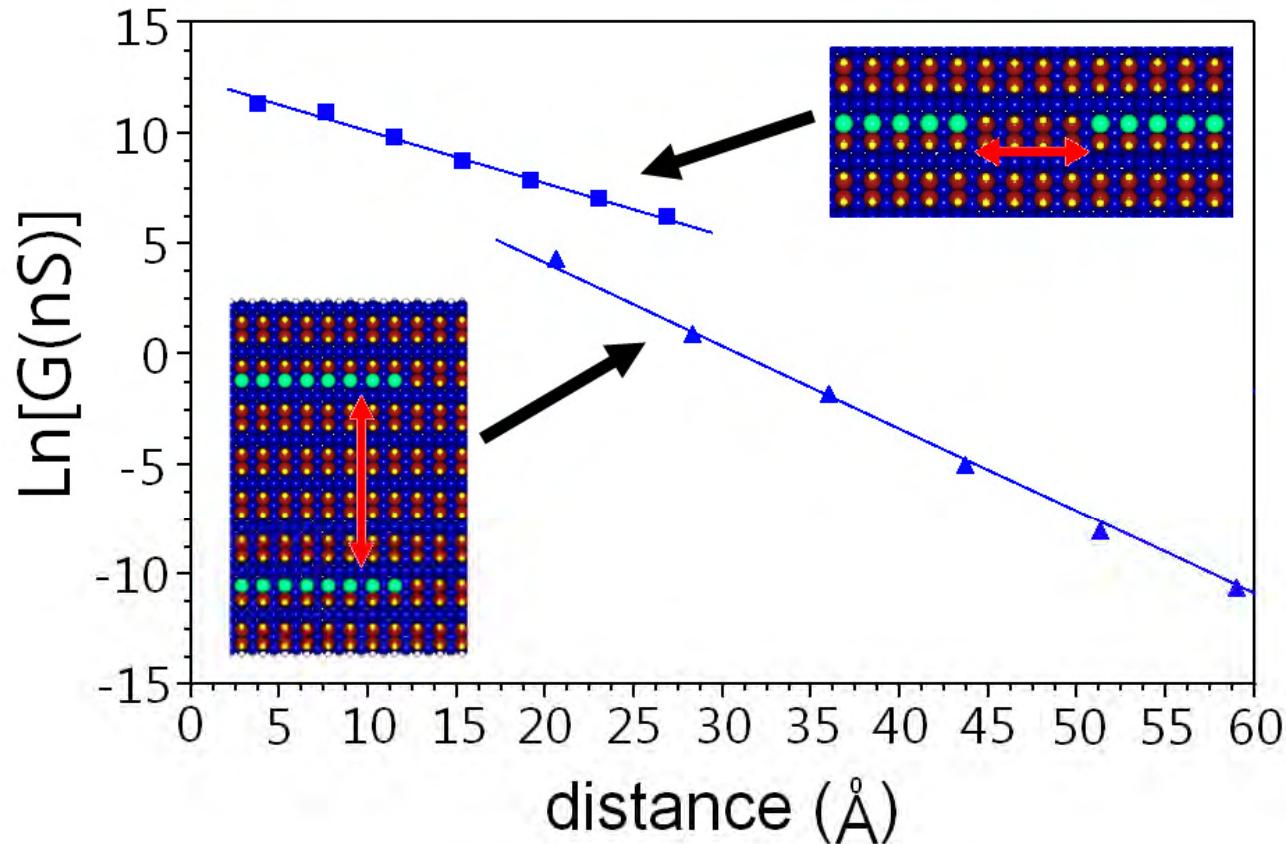






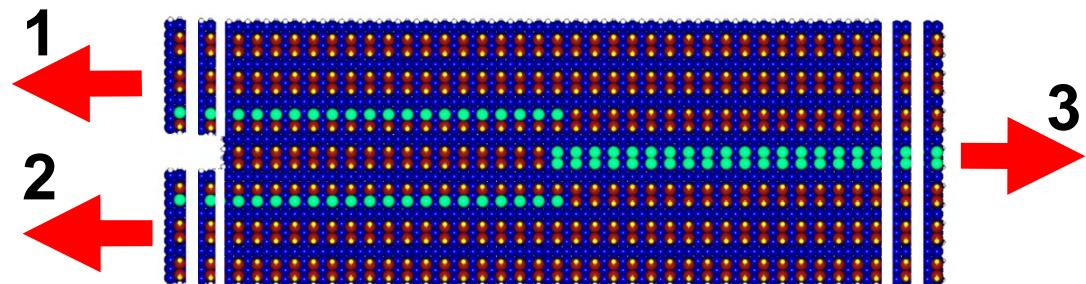


-Theoretical optimization of surface circuits on passivated semiconductors

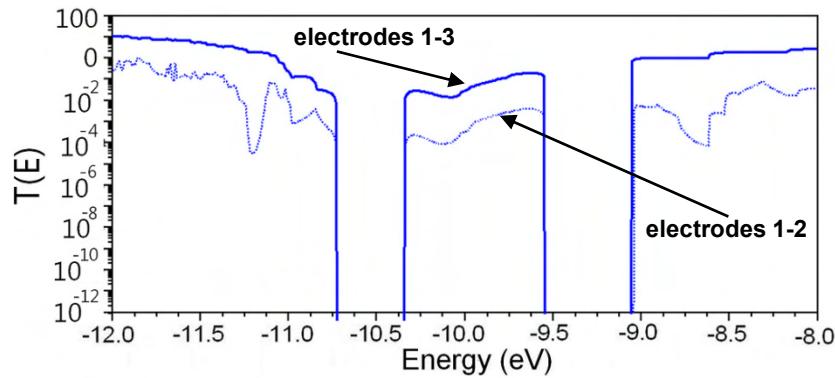


- electrons can tunnel through the surface between two separated atomic wires
- as a tunnelling process, the conductance decay exponentially with the distance

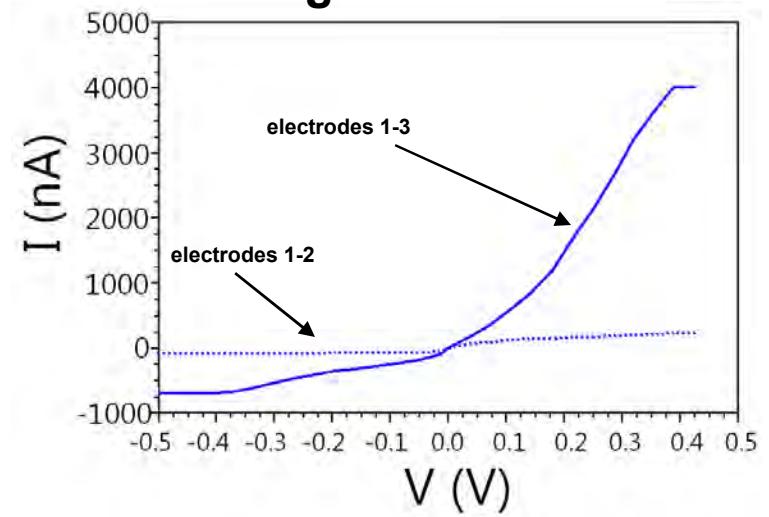
OR gate



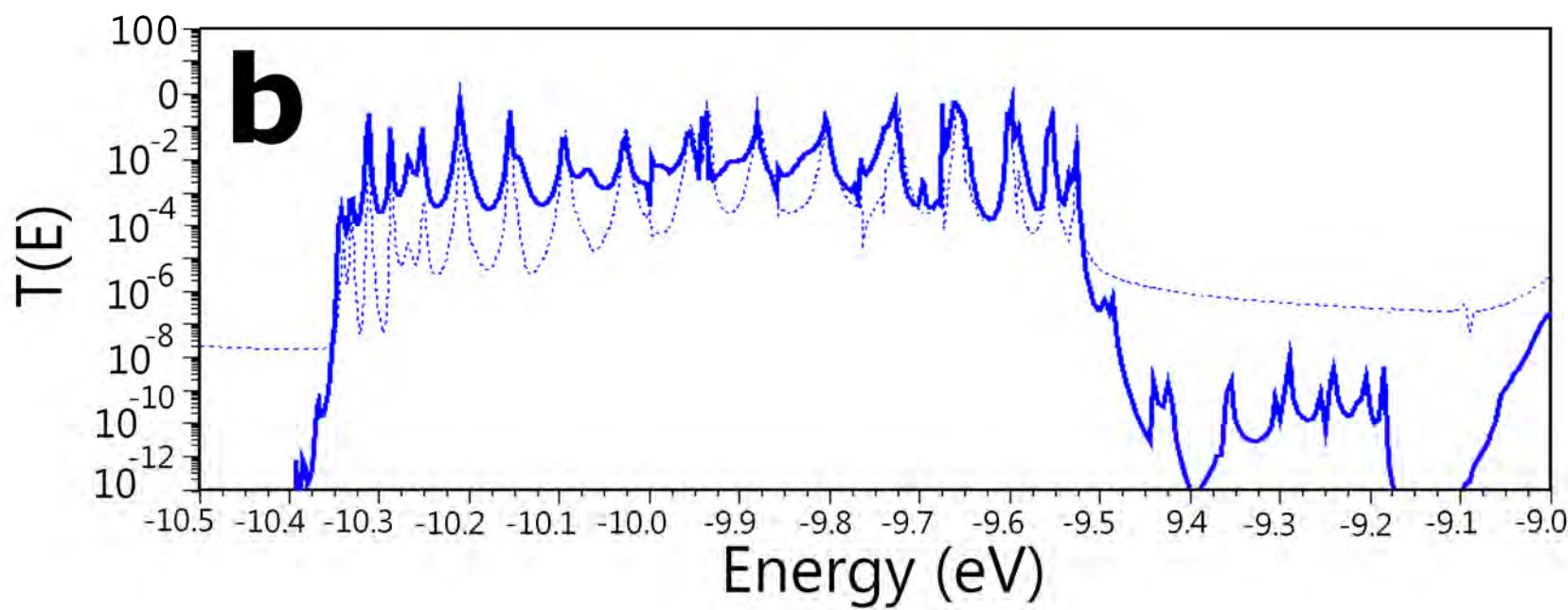
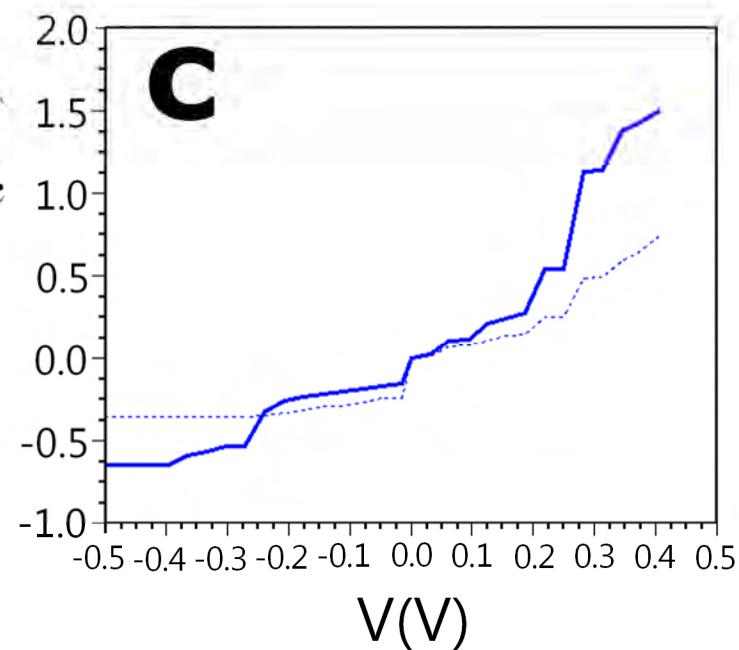
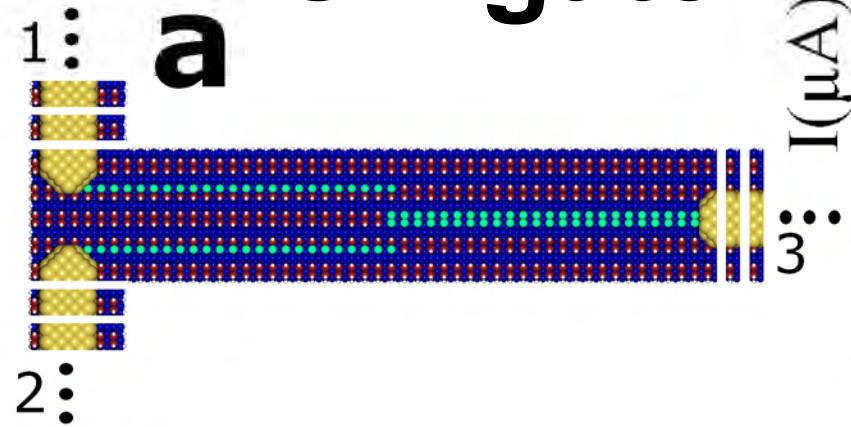
Transmission spectrum



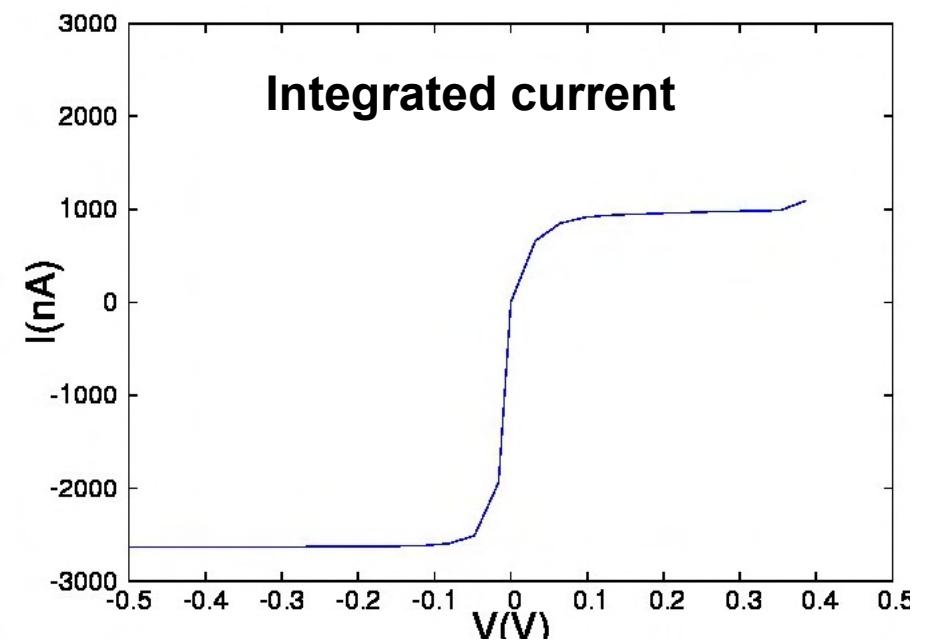
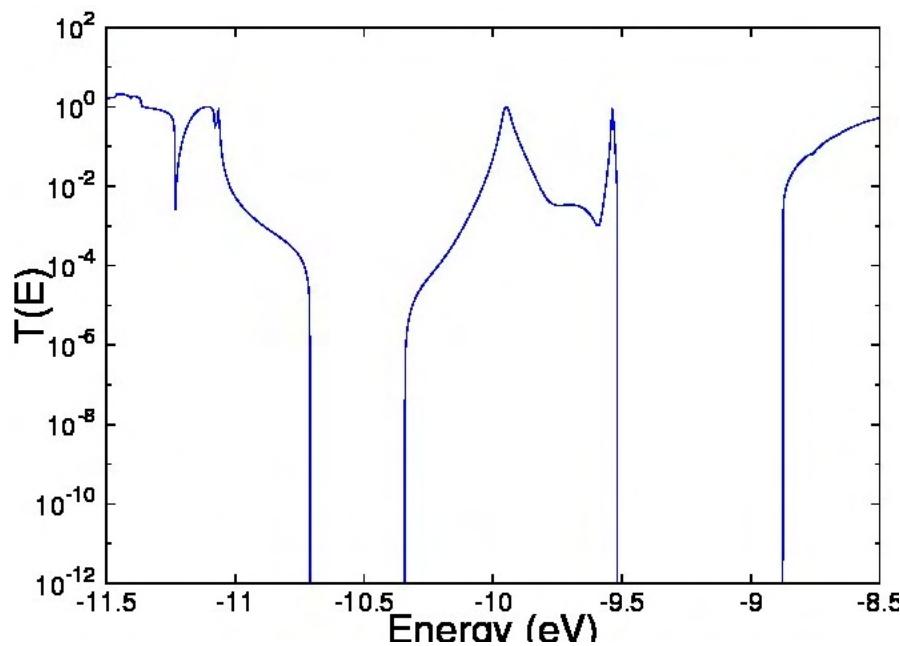
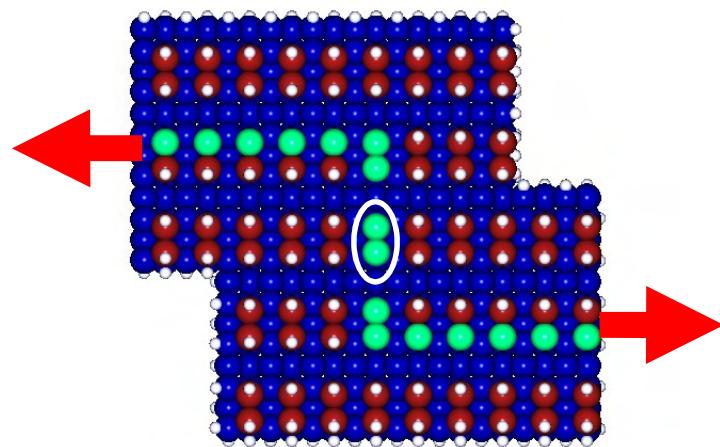
Integrated current



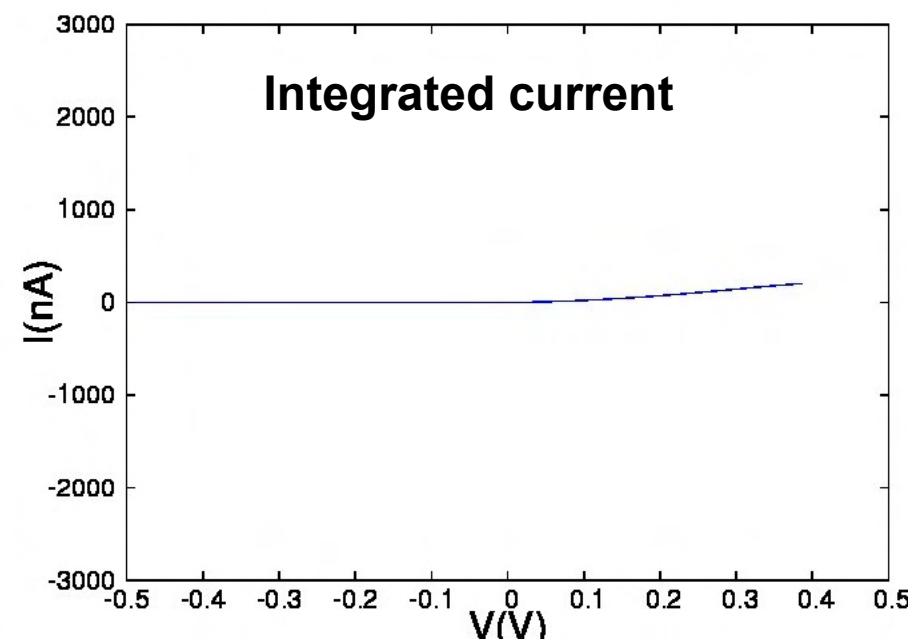
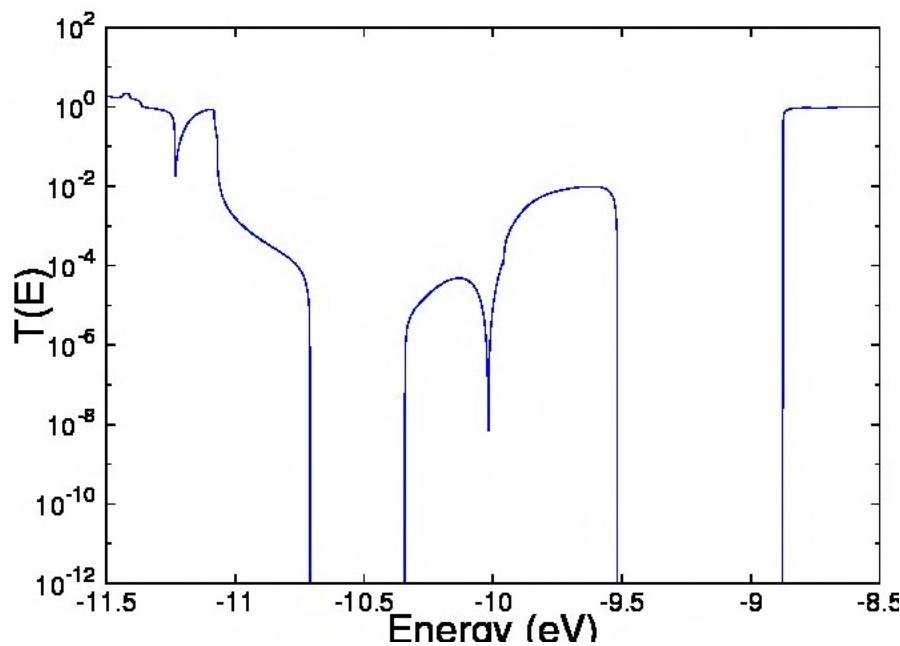
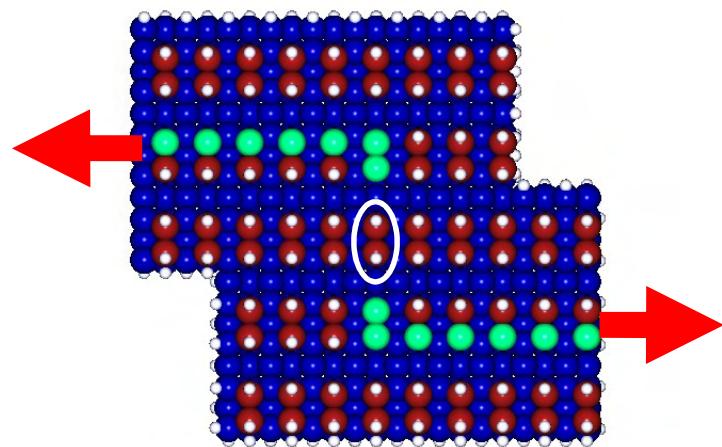
OR gate

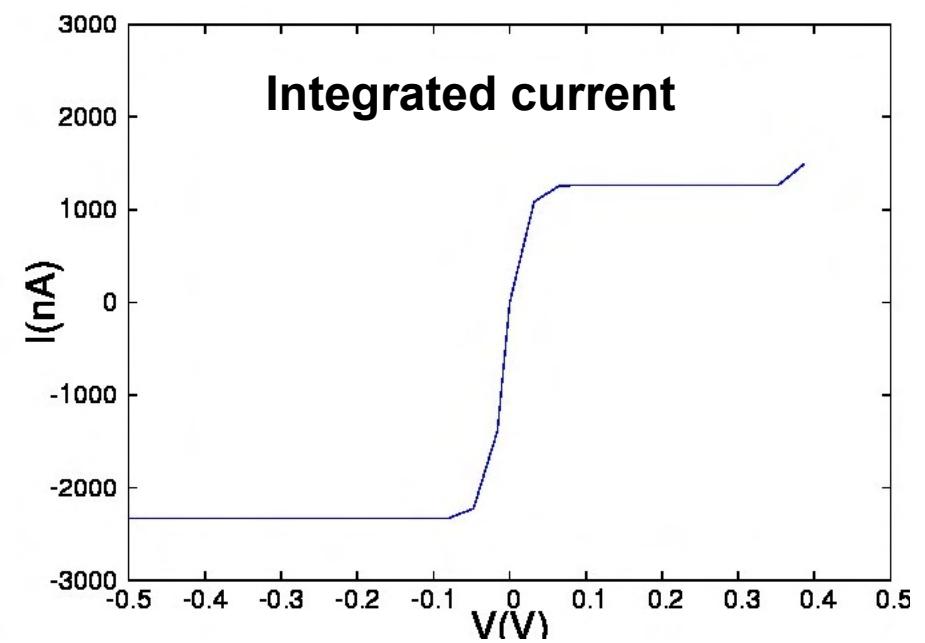
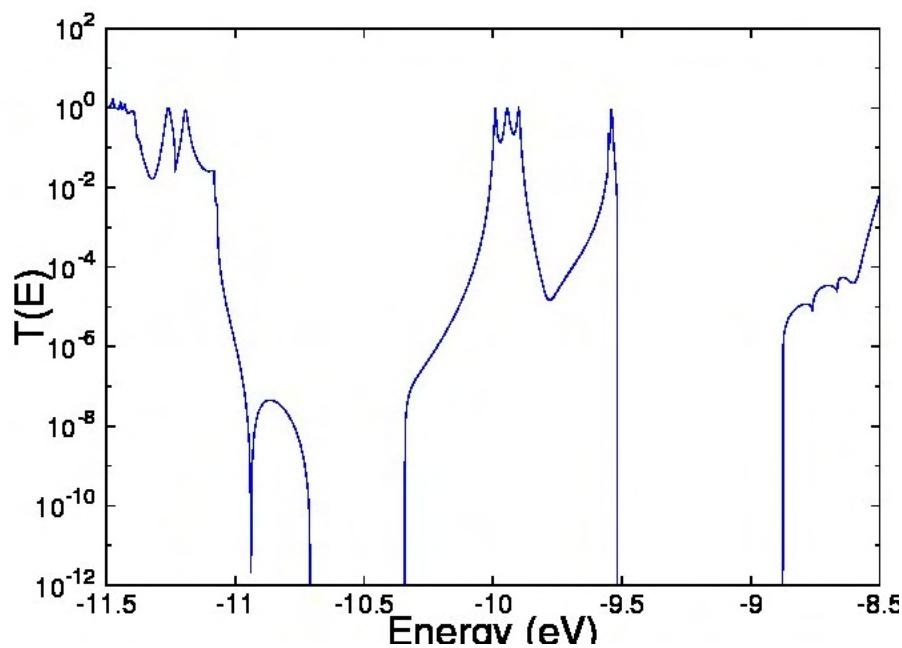
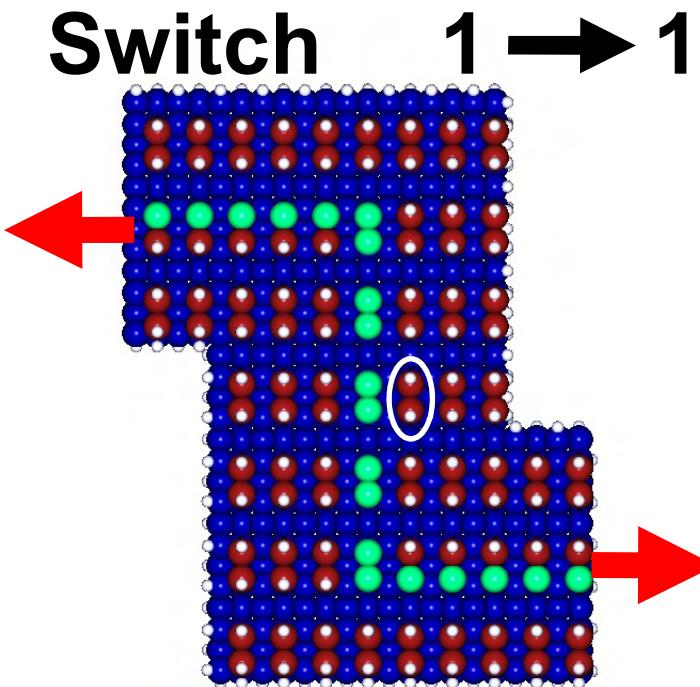


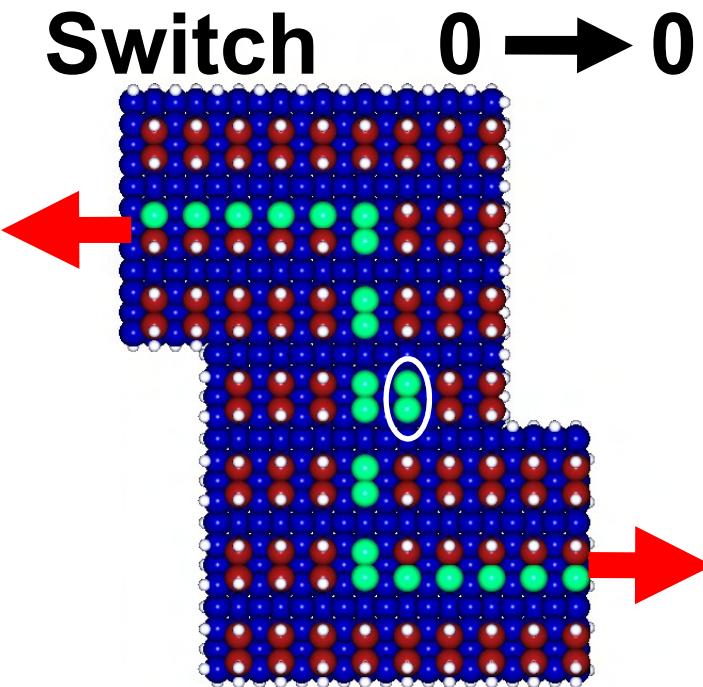
Switch $0 \rightarrow 1$ **INVERTER**



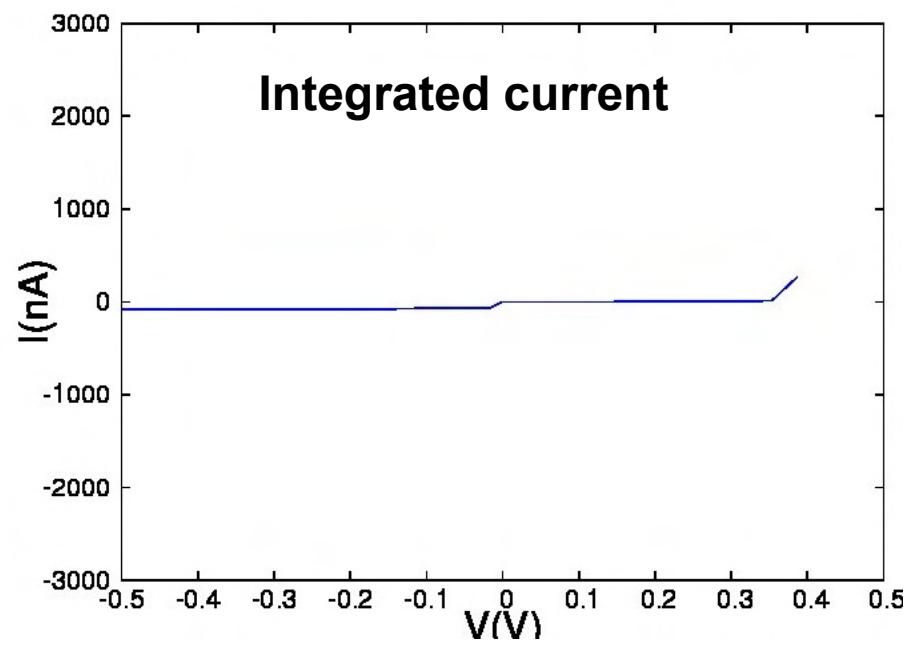
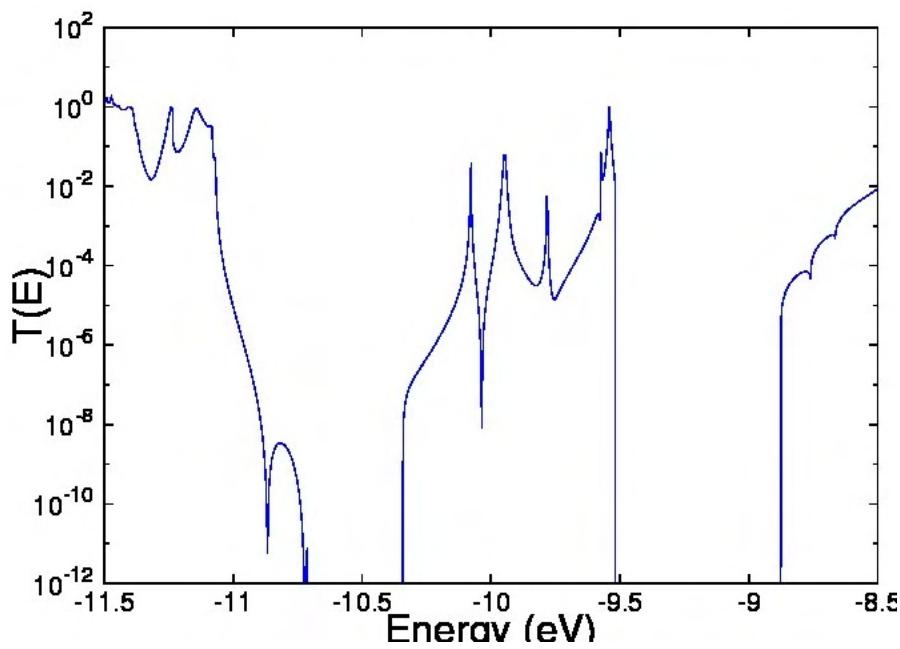
Switch $1 \rightarrow 0$ **INVERTER**



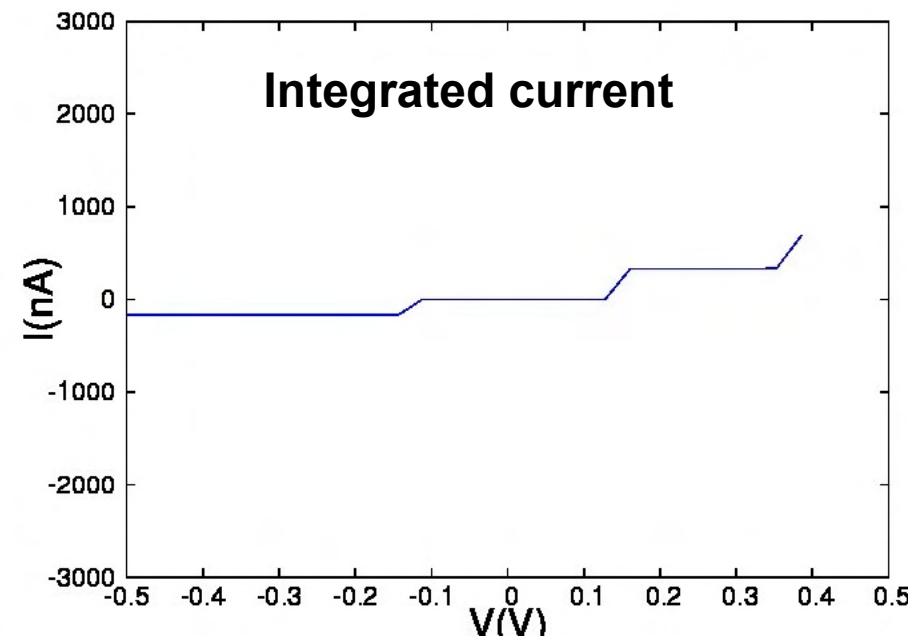
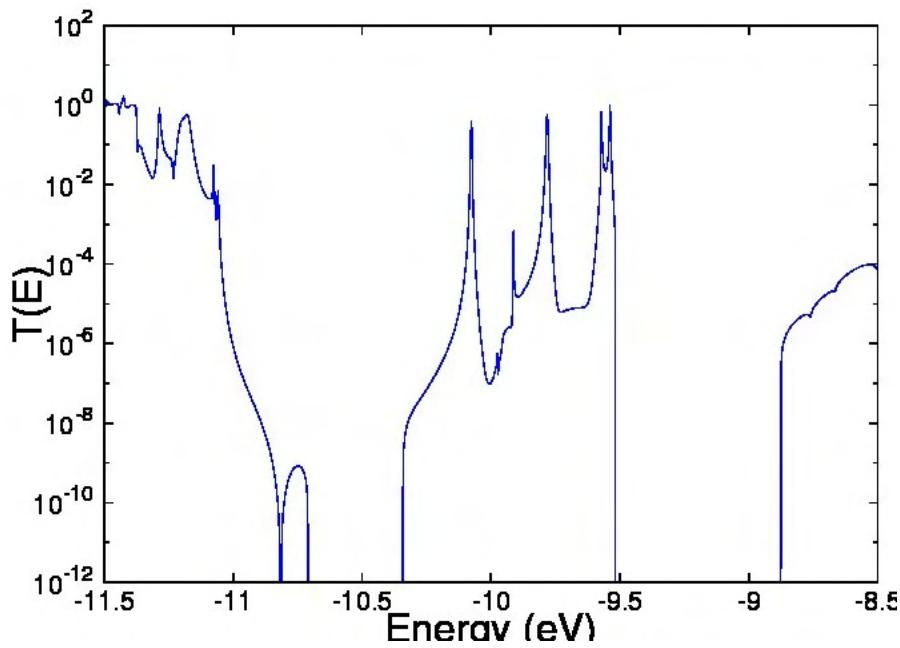
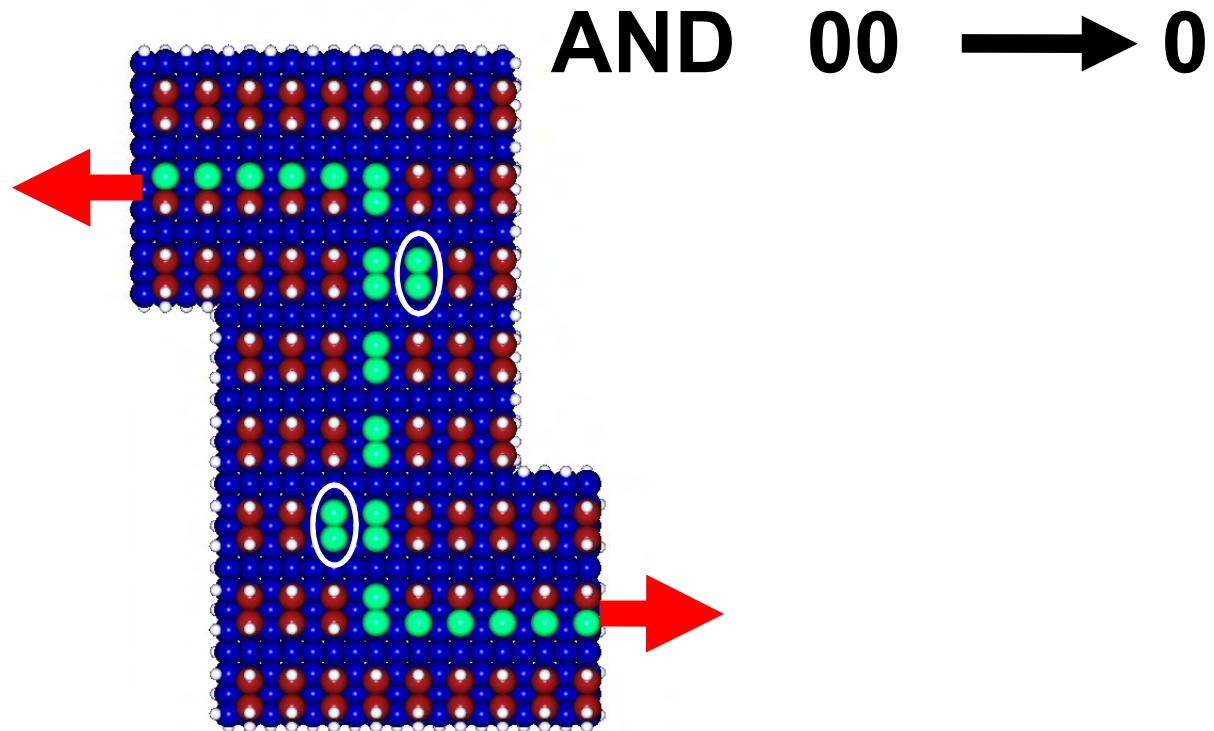


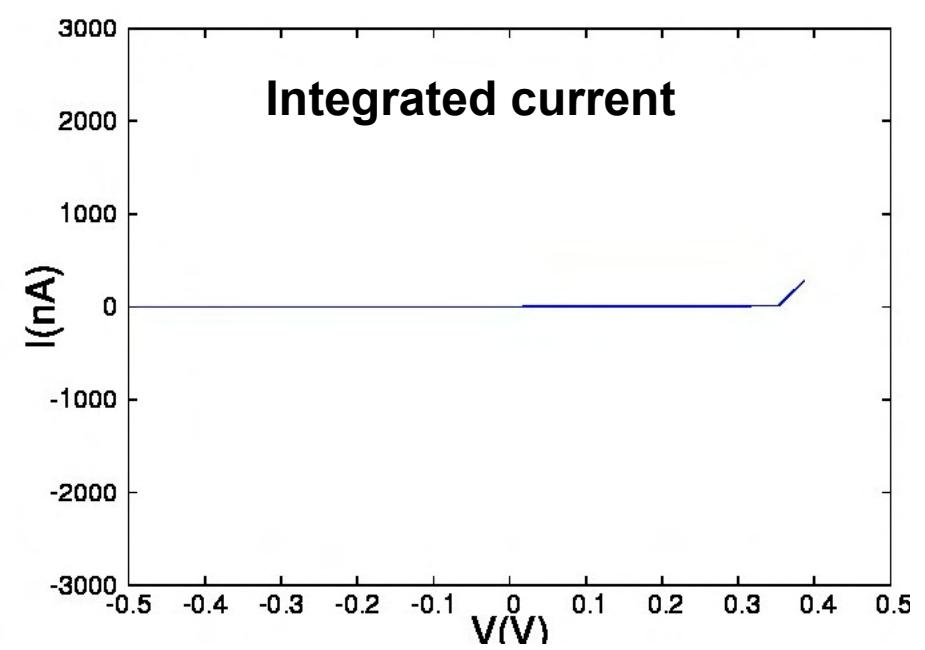
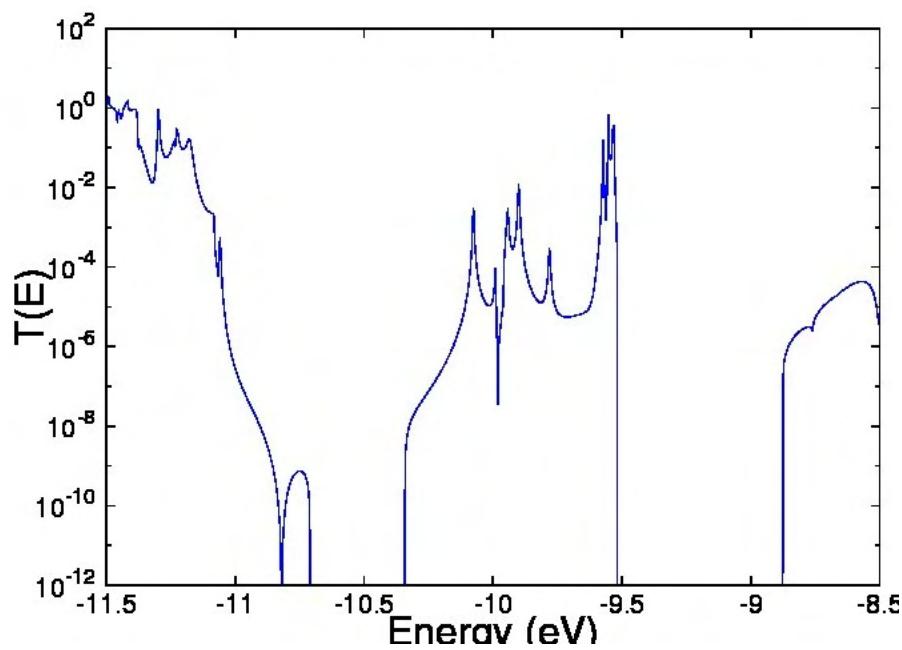
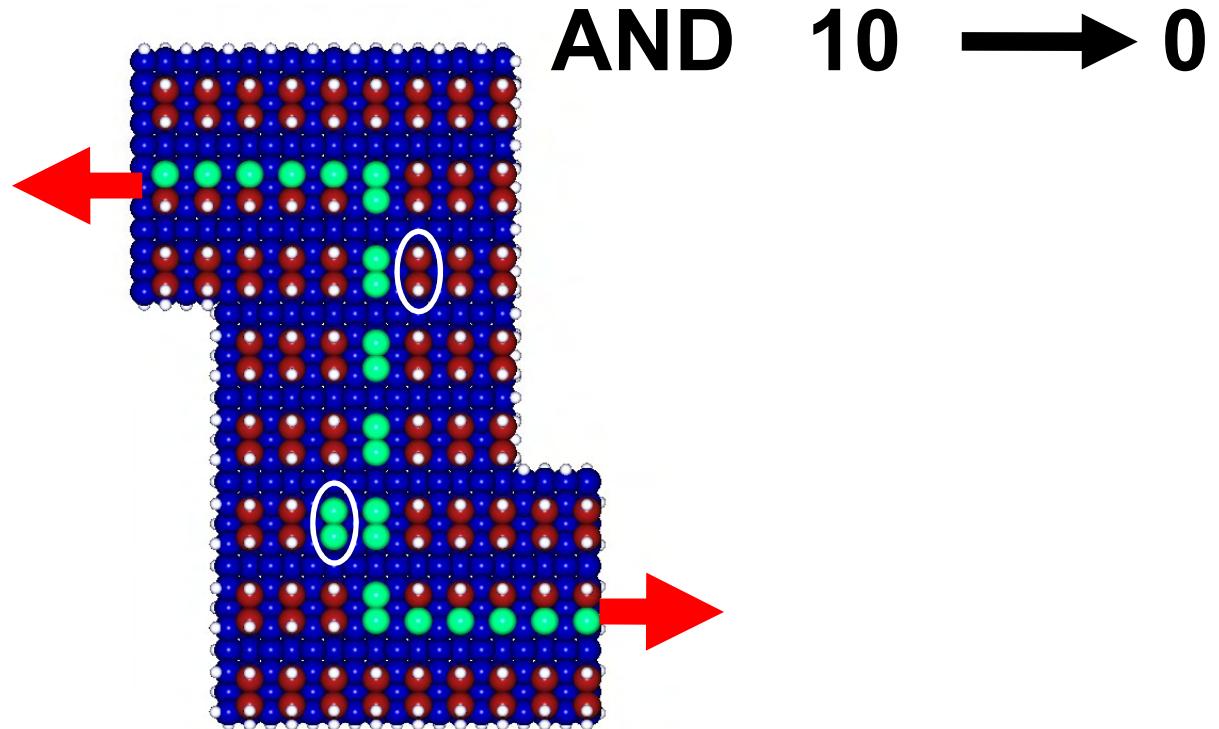


FOLLOWER

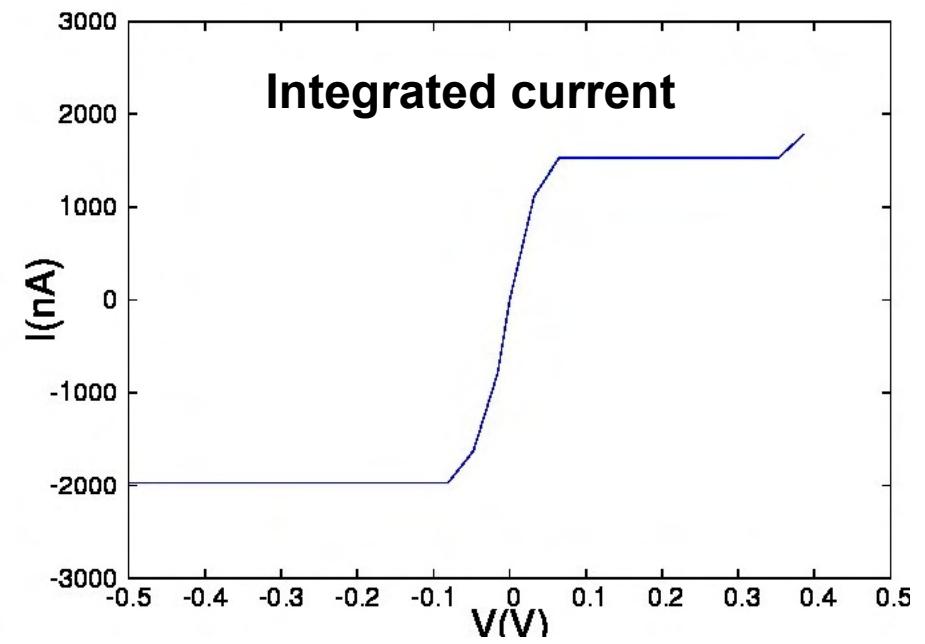
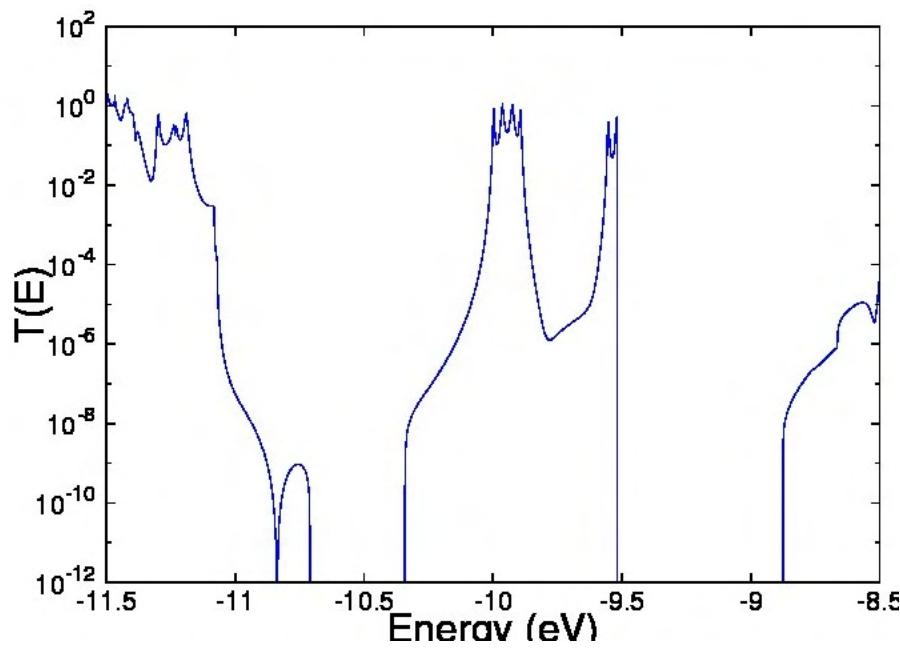
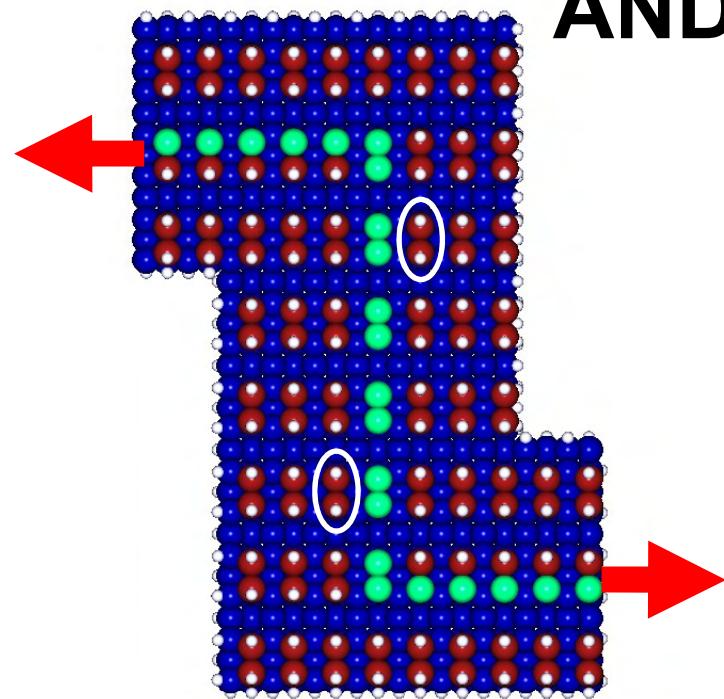


AND



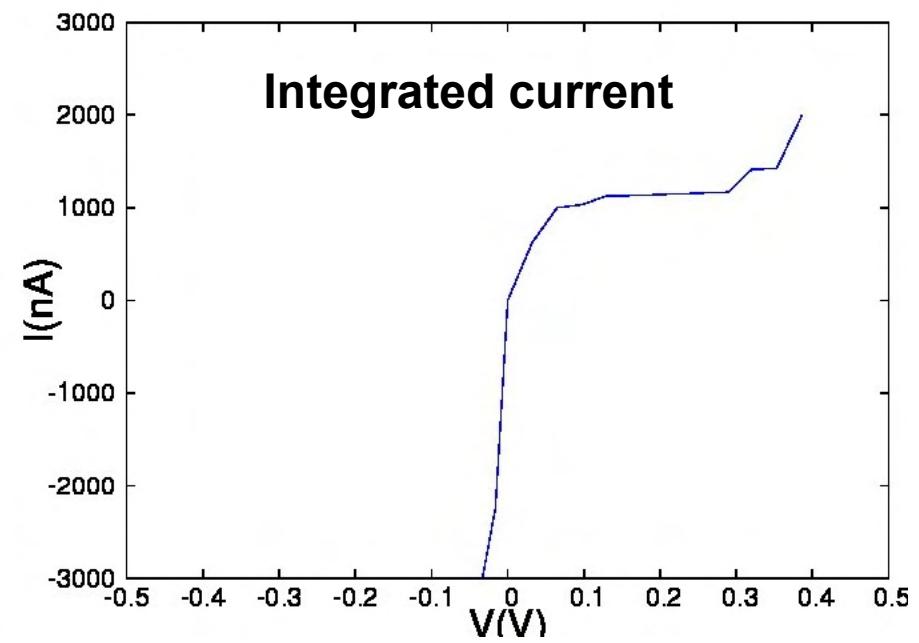
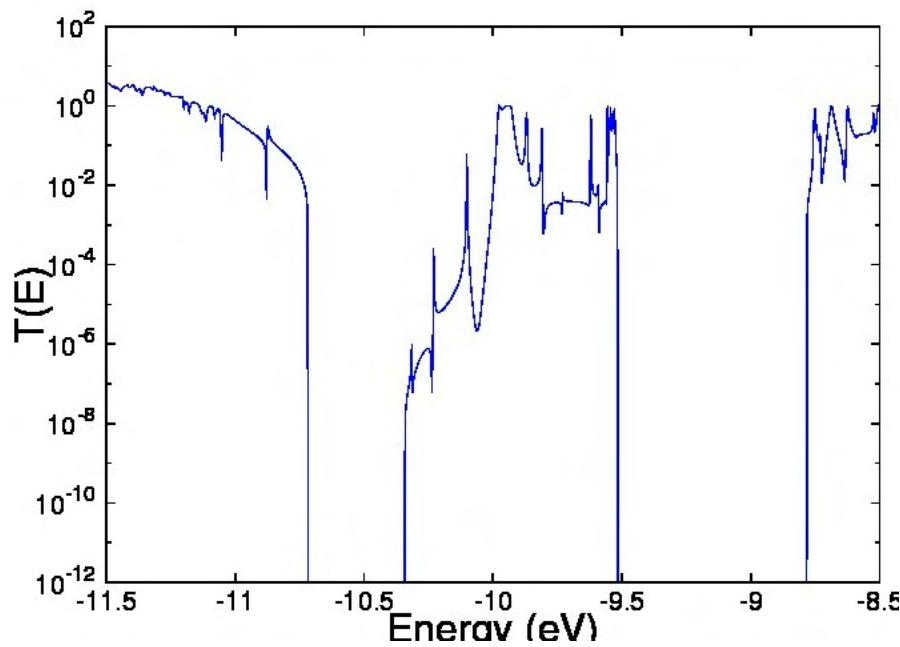
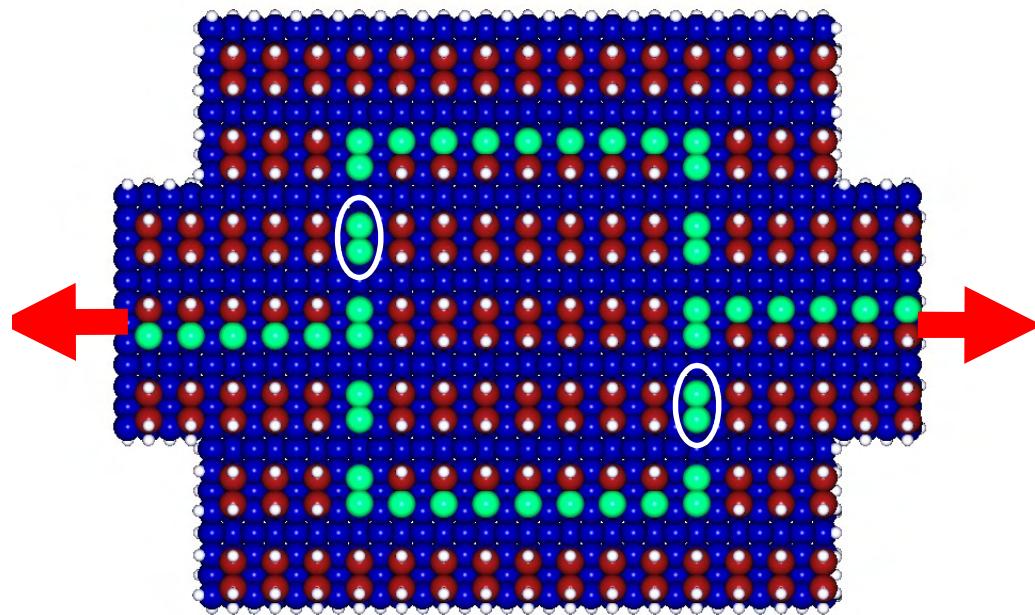


AND 11 → 1

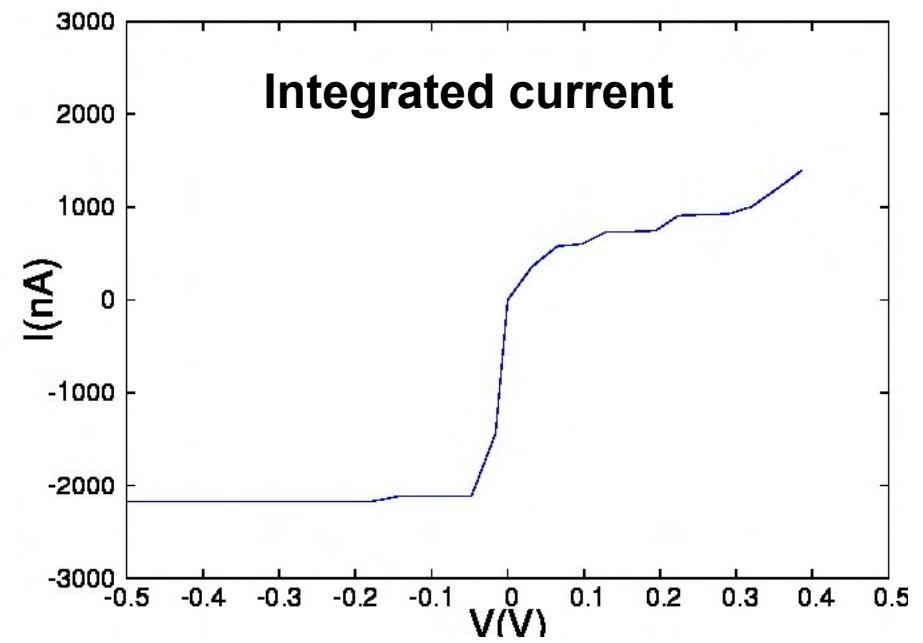
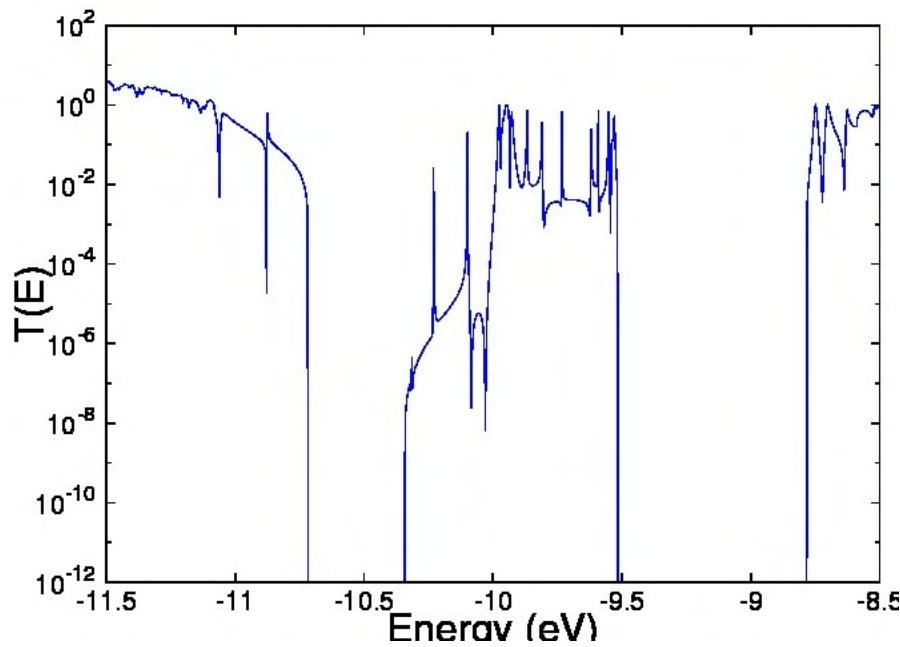
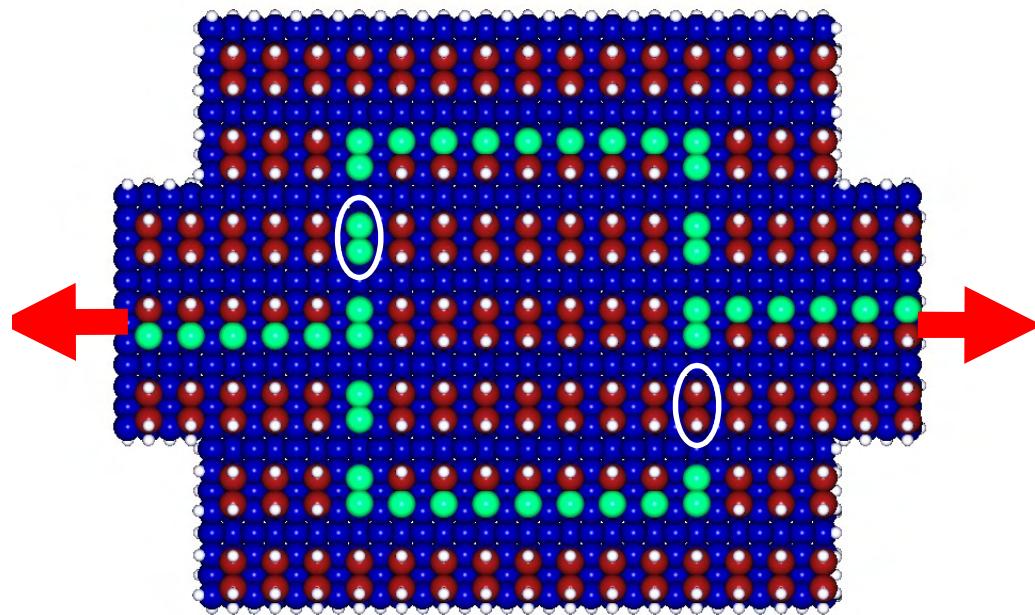


NAND

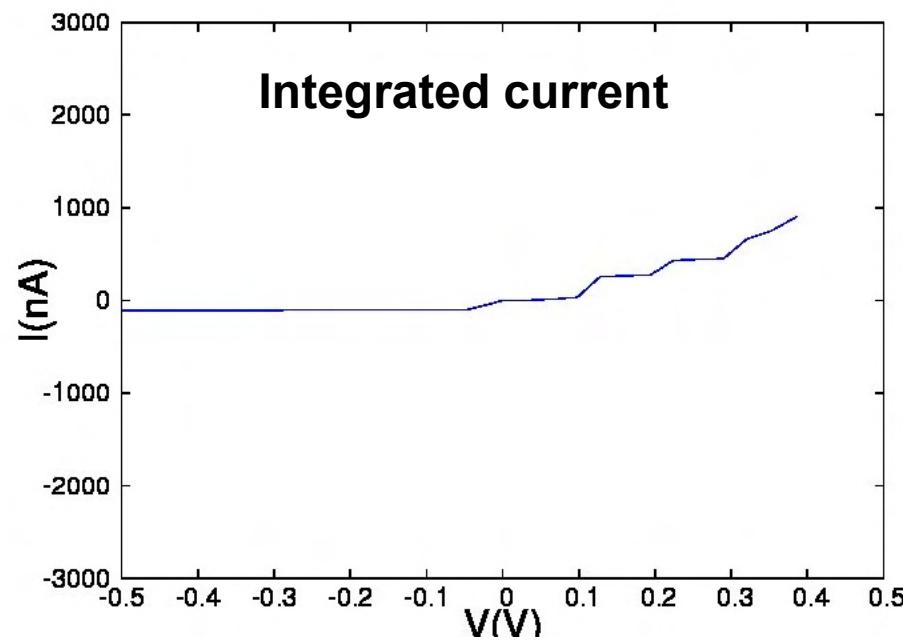
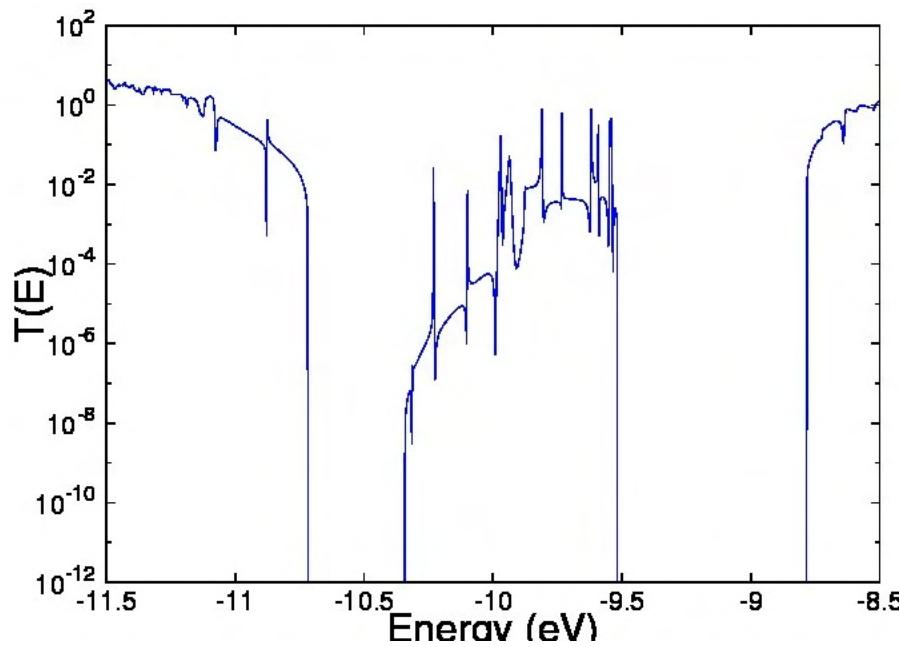
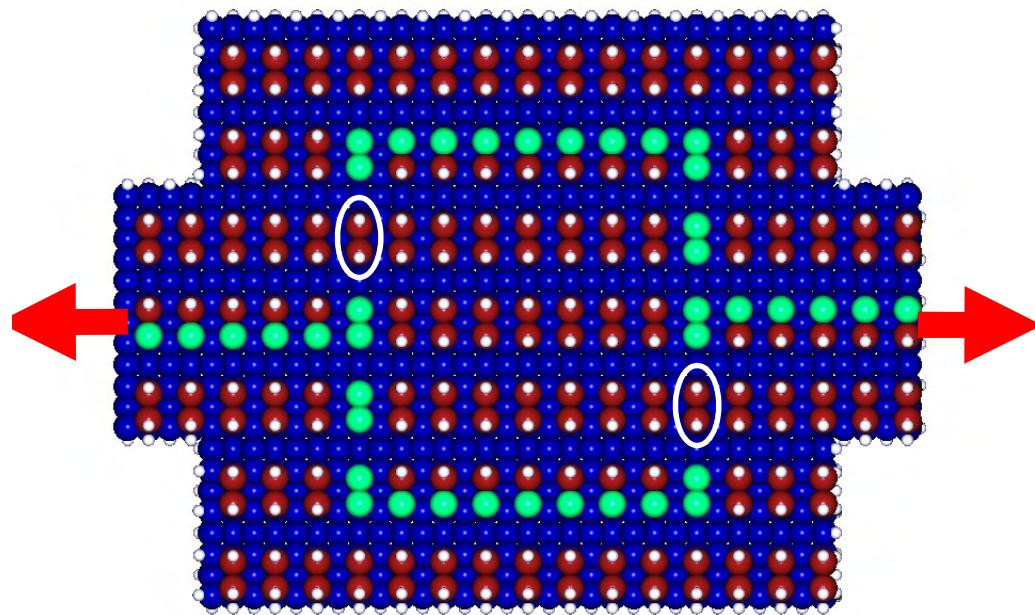
NAND $00 \rightarrow 1$



NAND $10 \rightarrow 1$

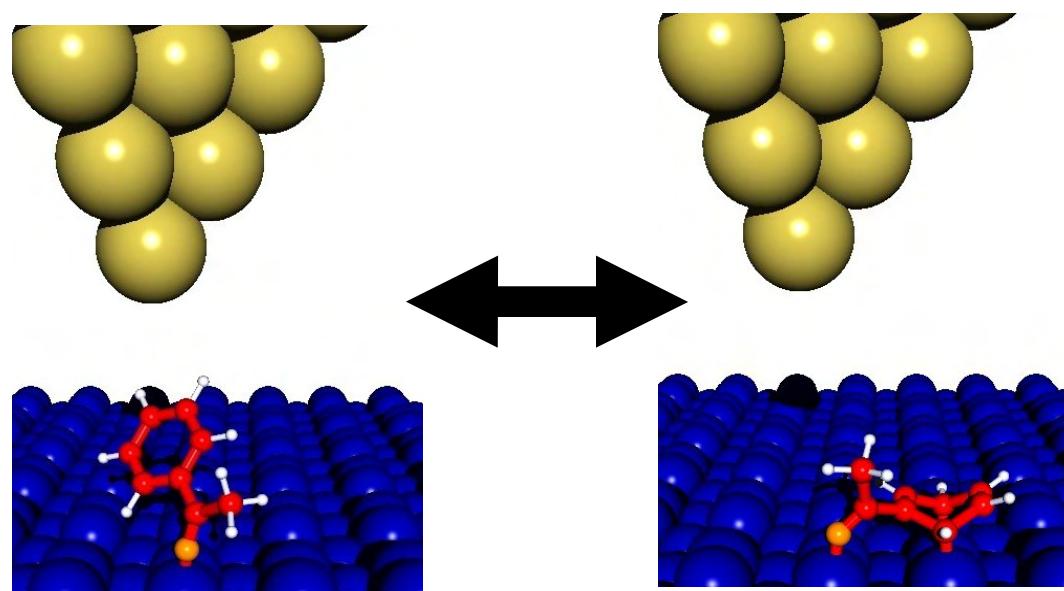


NAND 11 → 0



-Theoretical optimization of surface circuits on passivated semiconductors

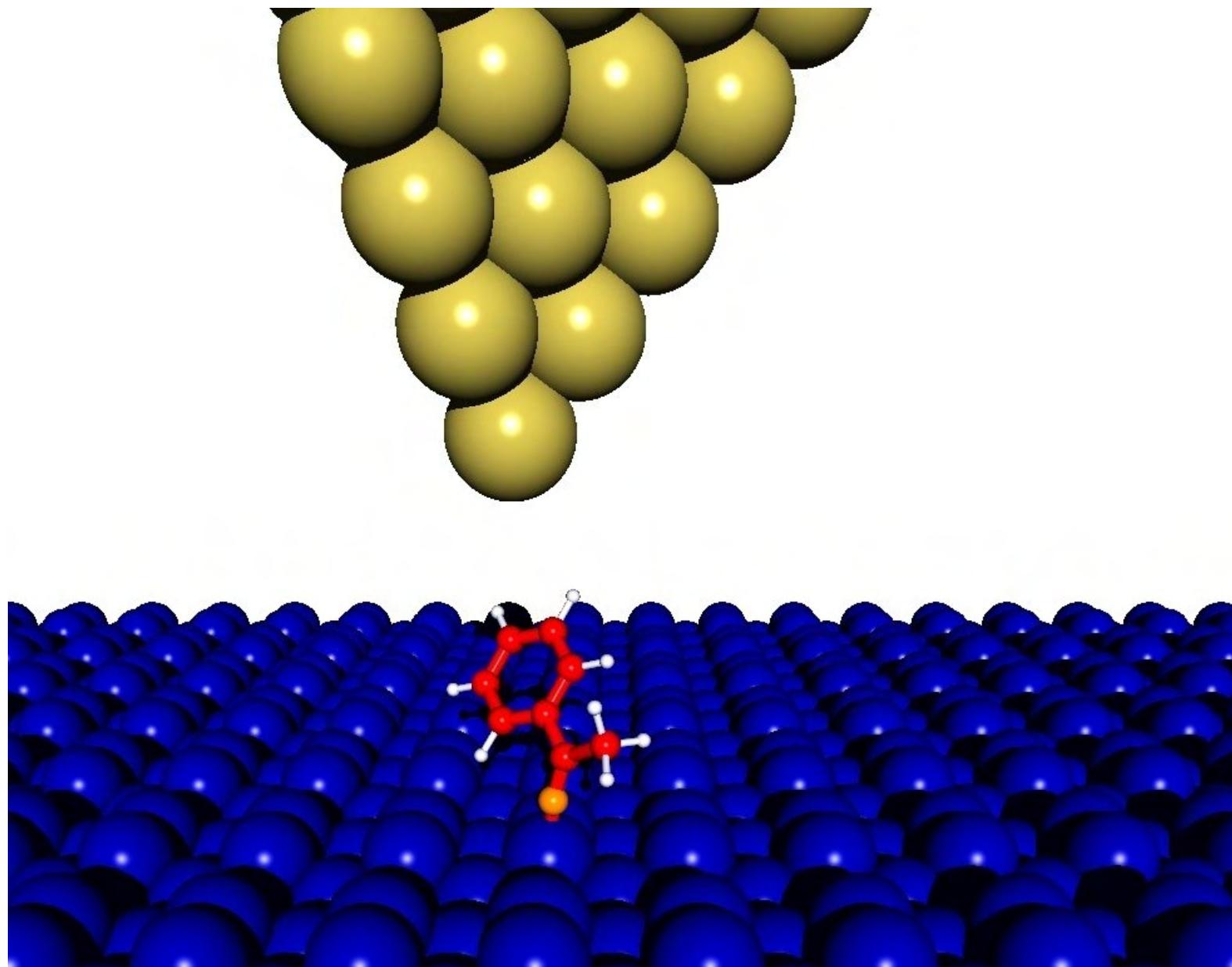
Switch



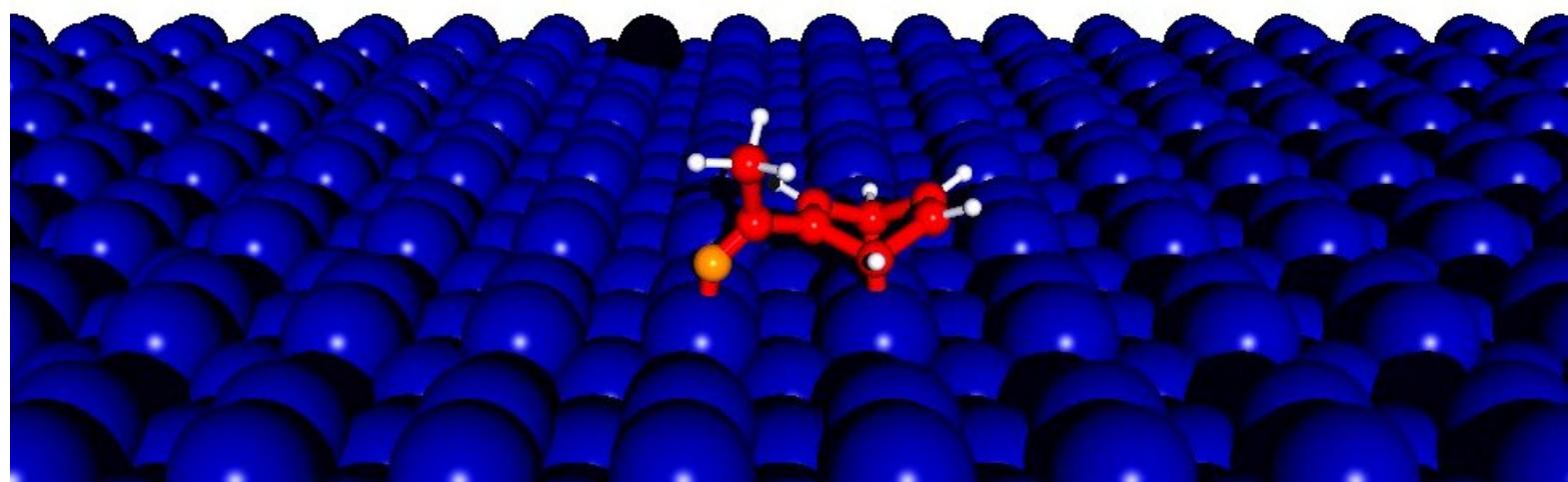
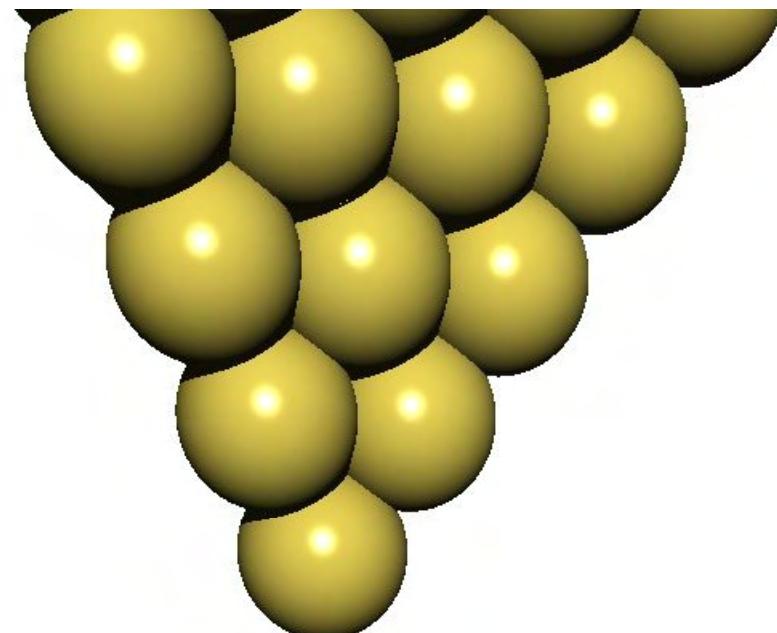
optimized structures with
ASED+ approach

Acetophenone can switch on Si(100) by the inelastic forces of the STM current.

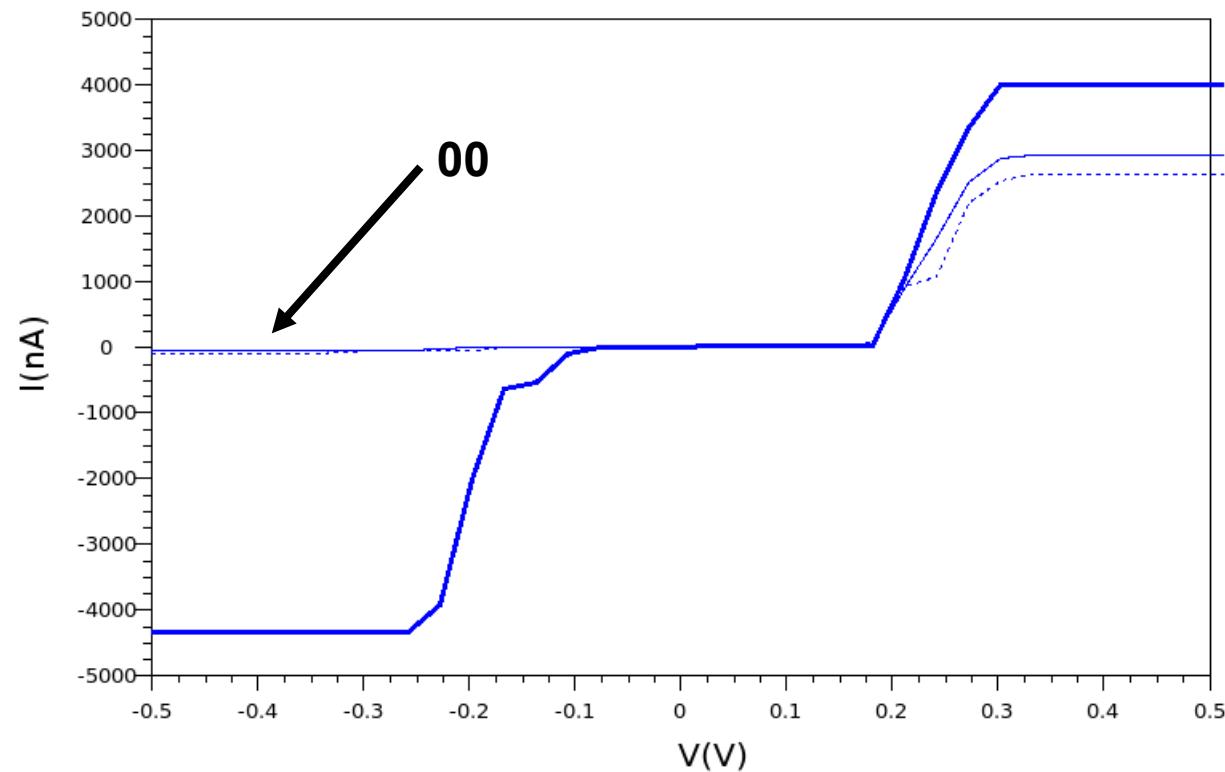
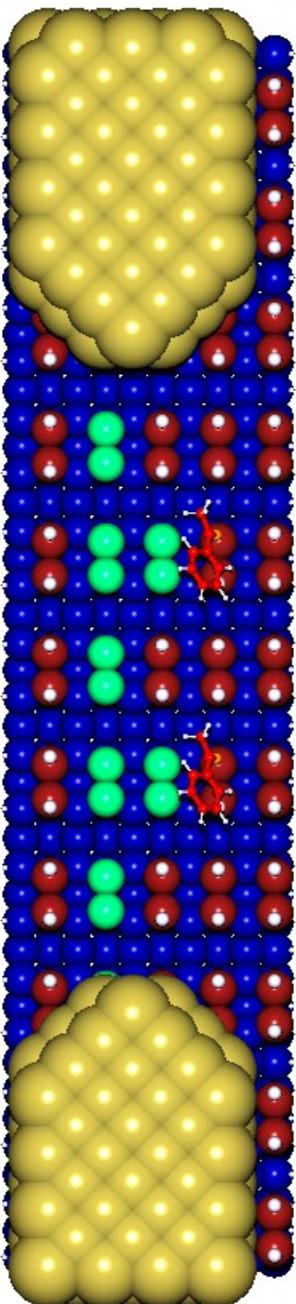
Acetophenone on Si(100)



Acetophenone on Si(100)

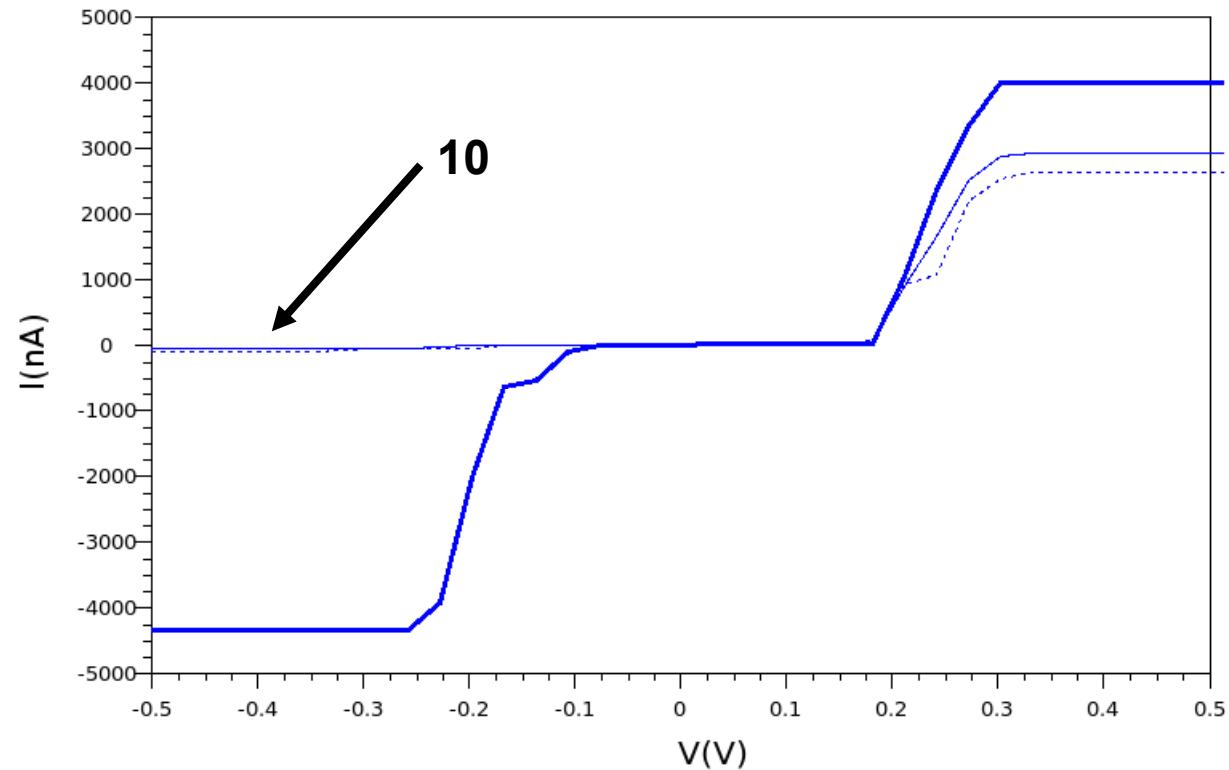
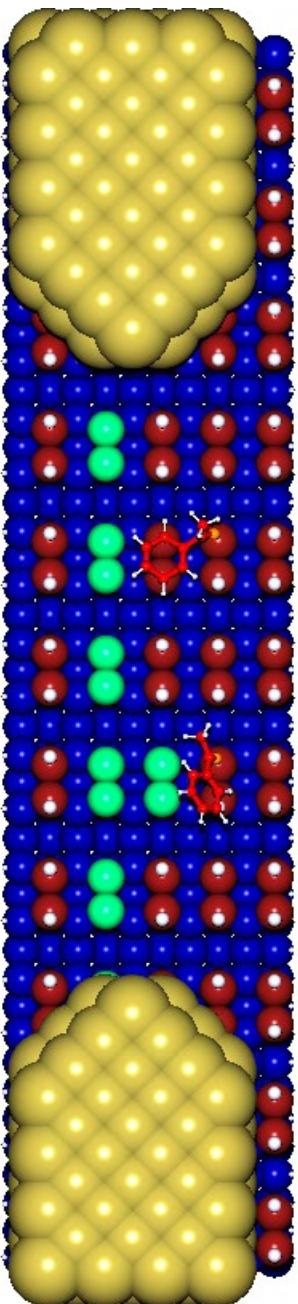


-Theoretical optimization of surface circuits on passivated semiconductors



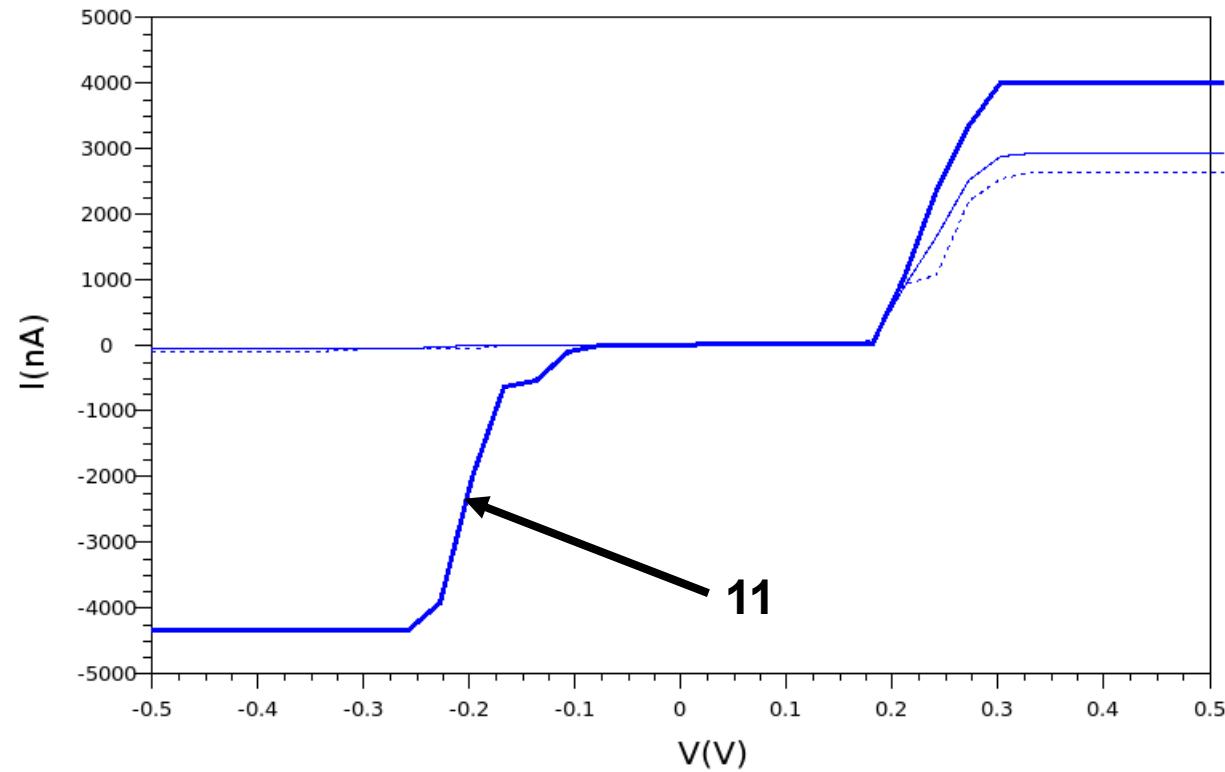
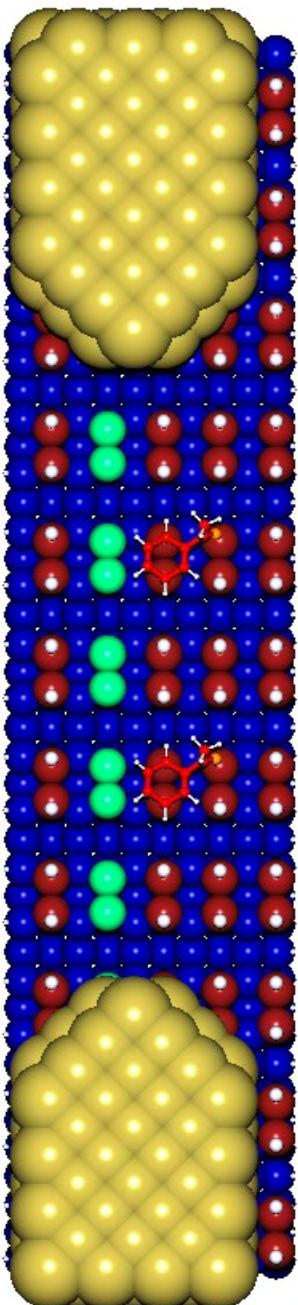
AND 00 → 0

-Theoretical optimization of surface circuits on passivated semiconductors

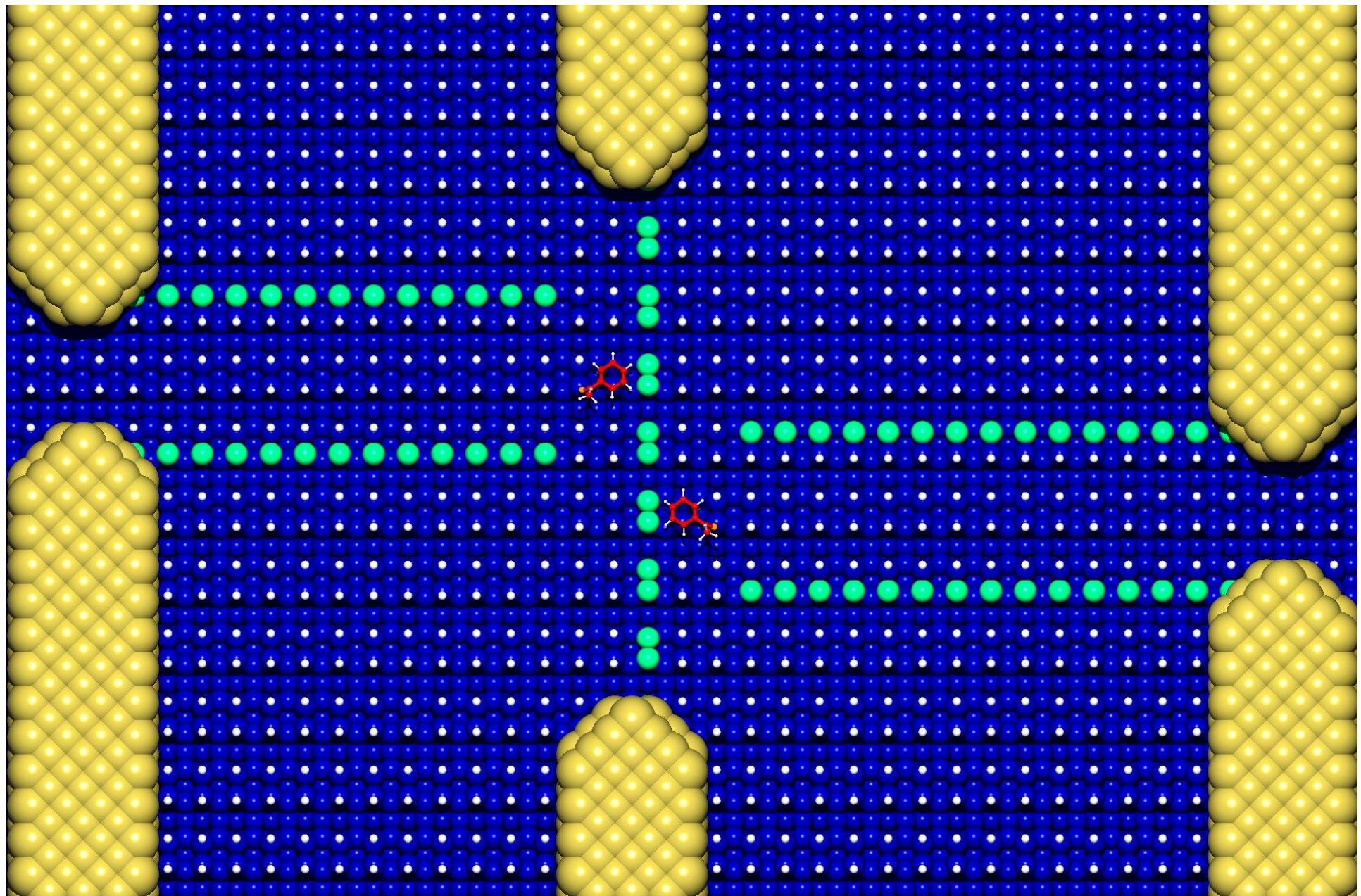


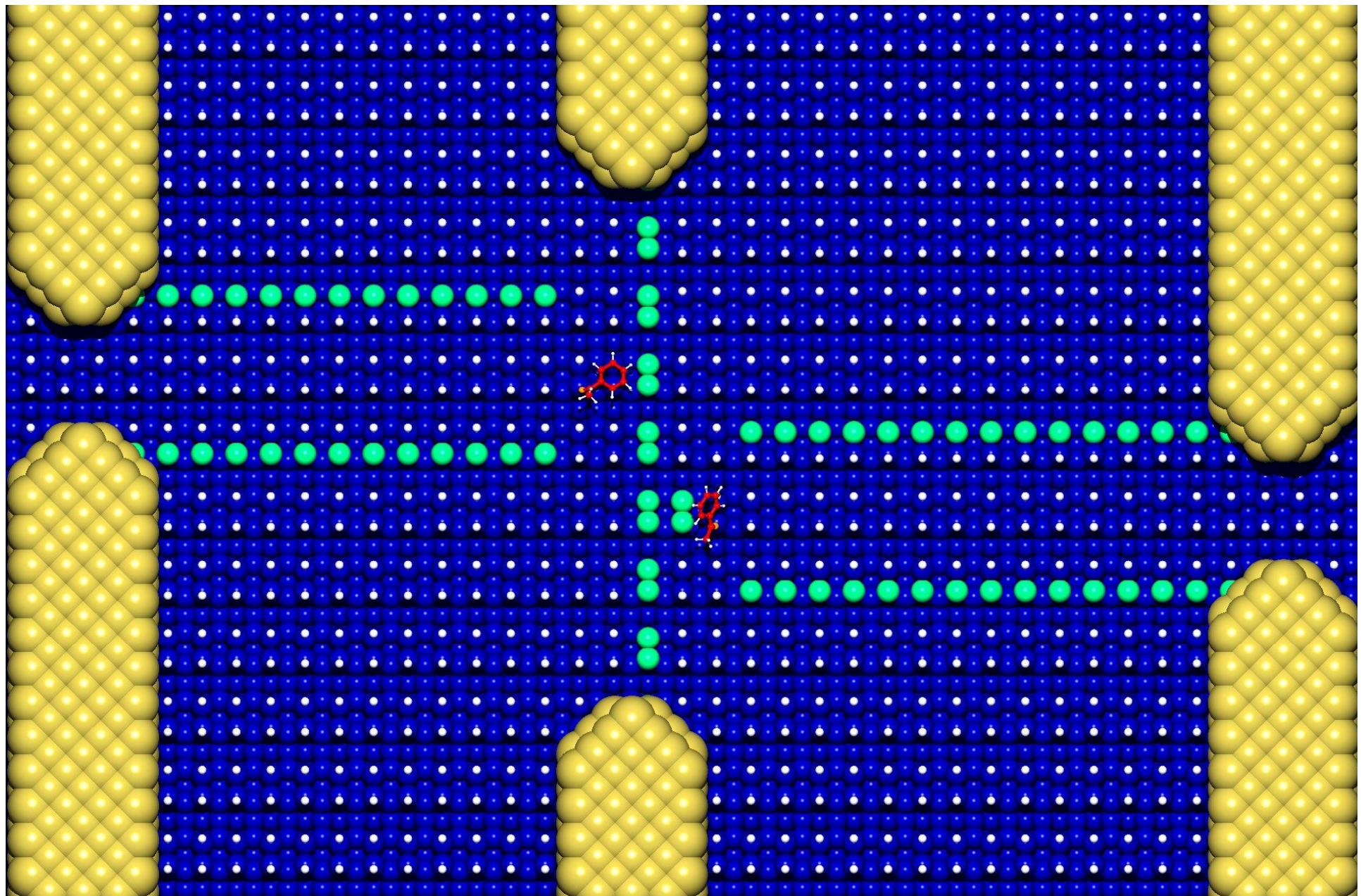
AND 10 → 0

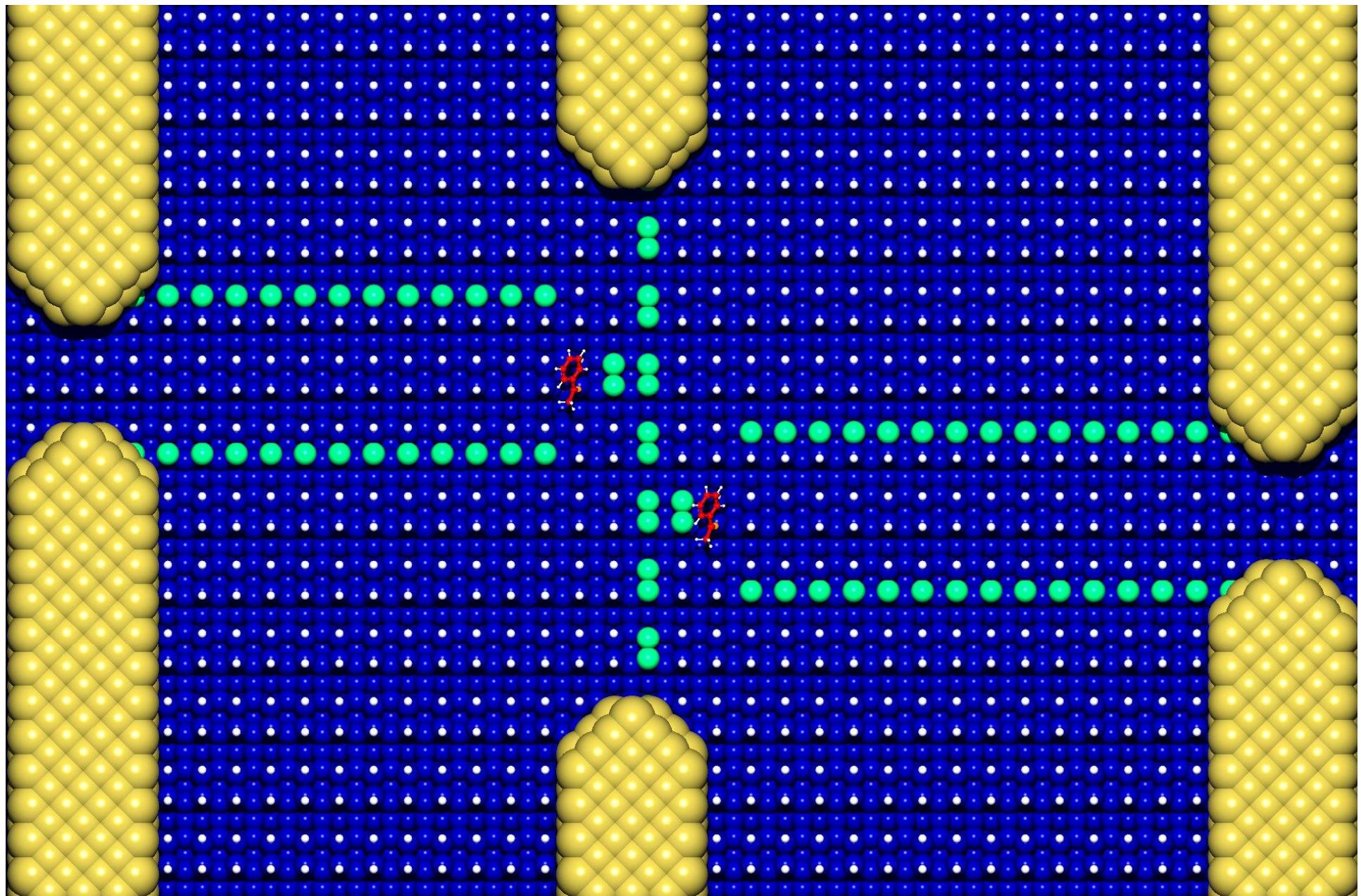
-Theoretical optimization of surface circuits on passivated semiconductors

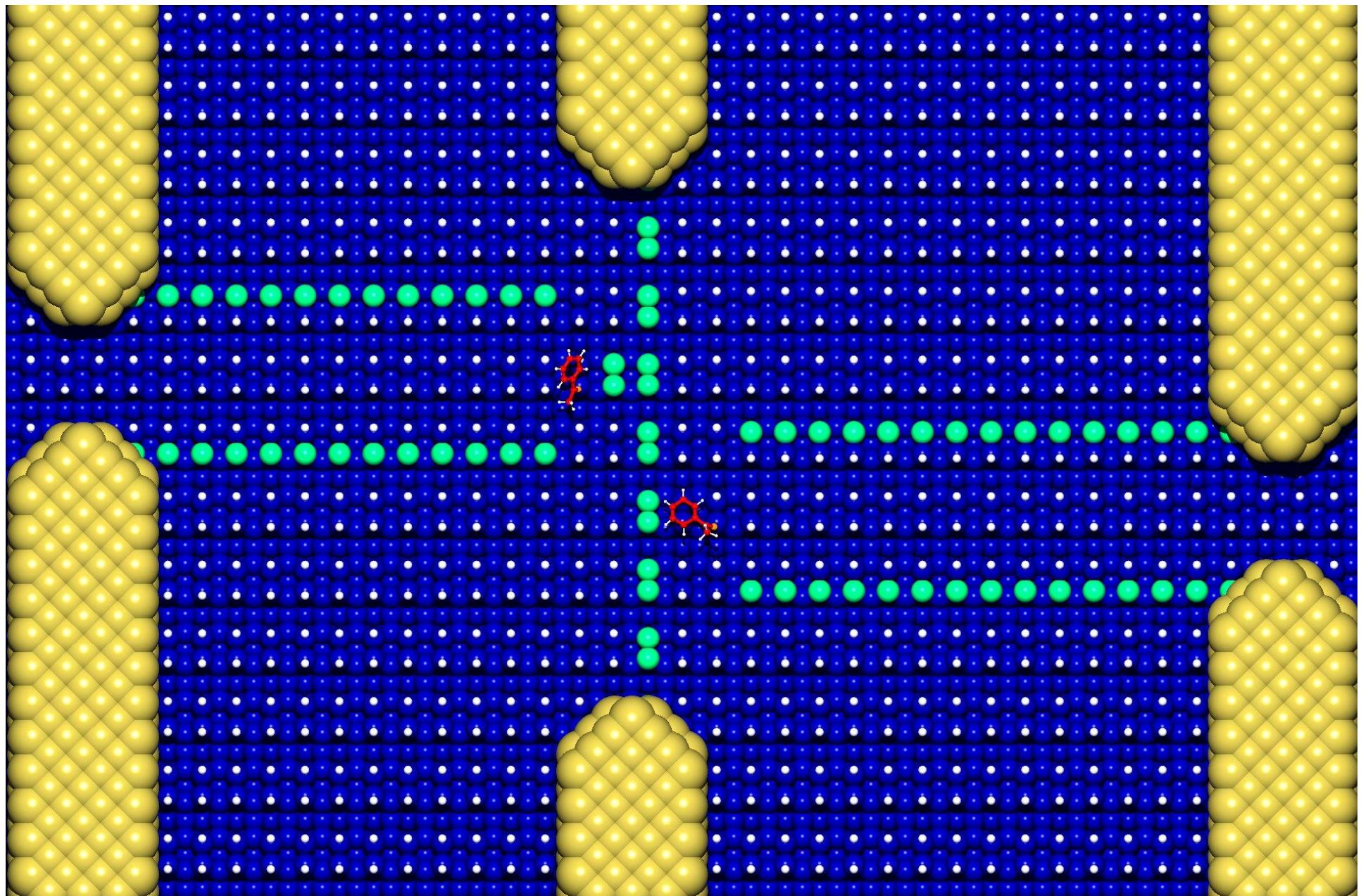


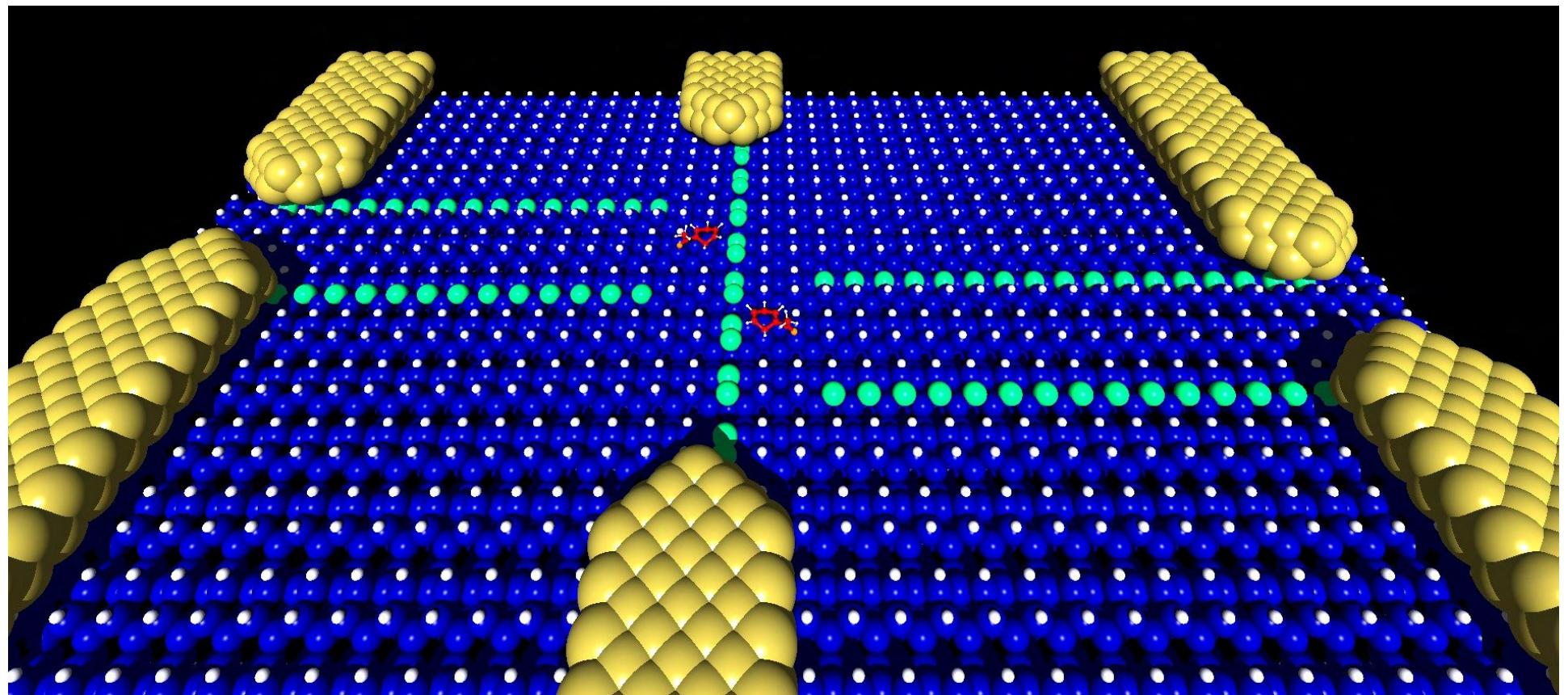
AND 11 → 1

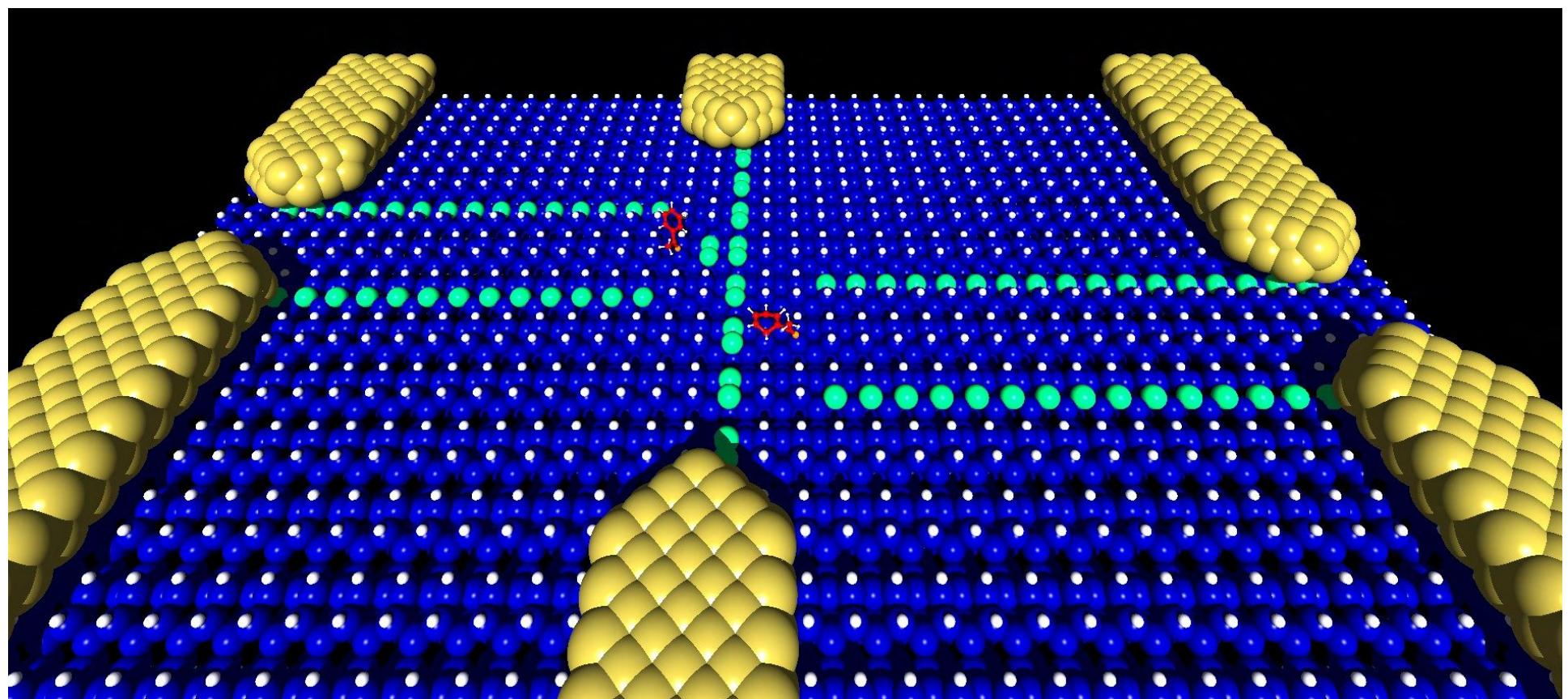


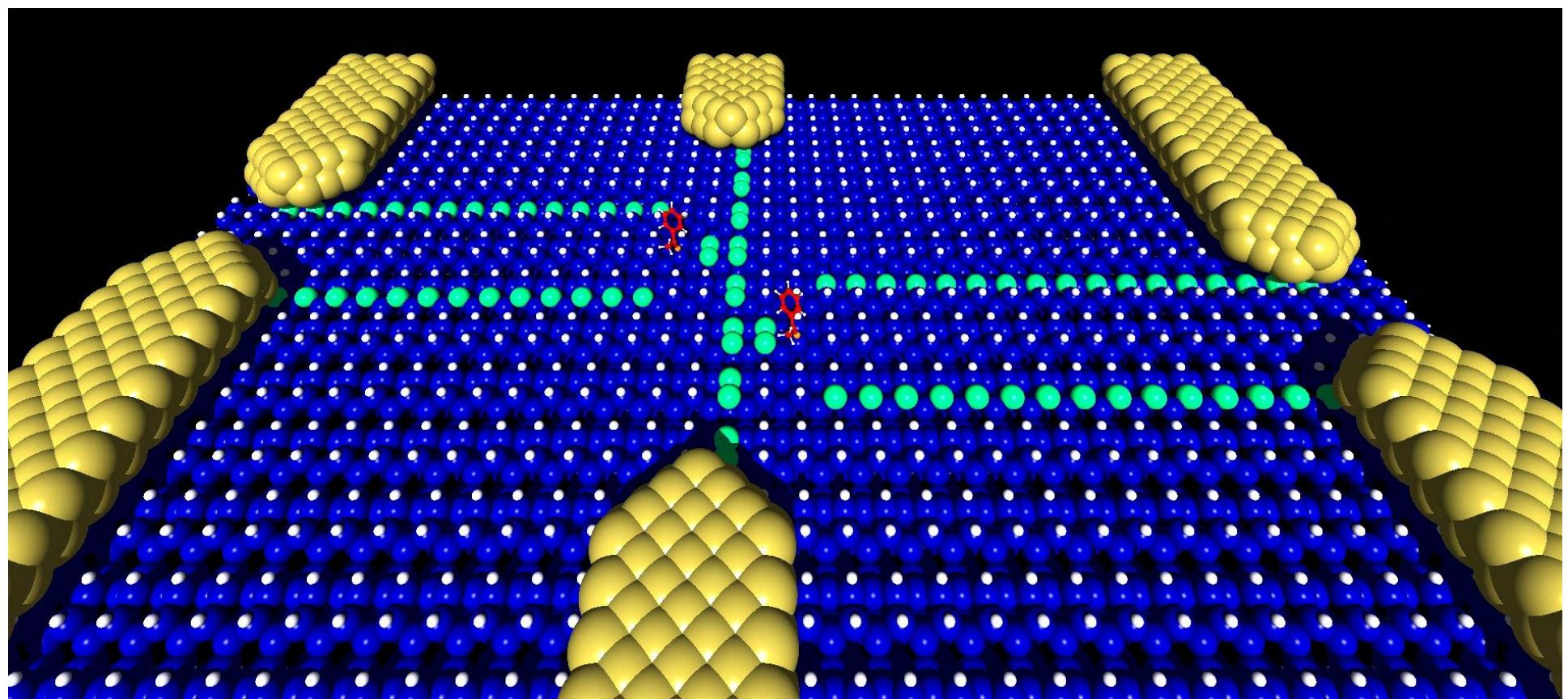


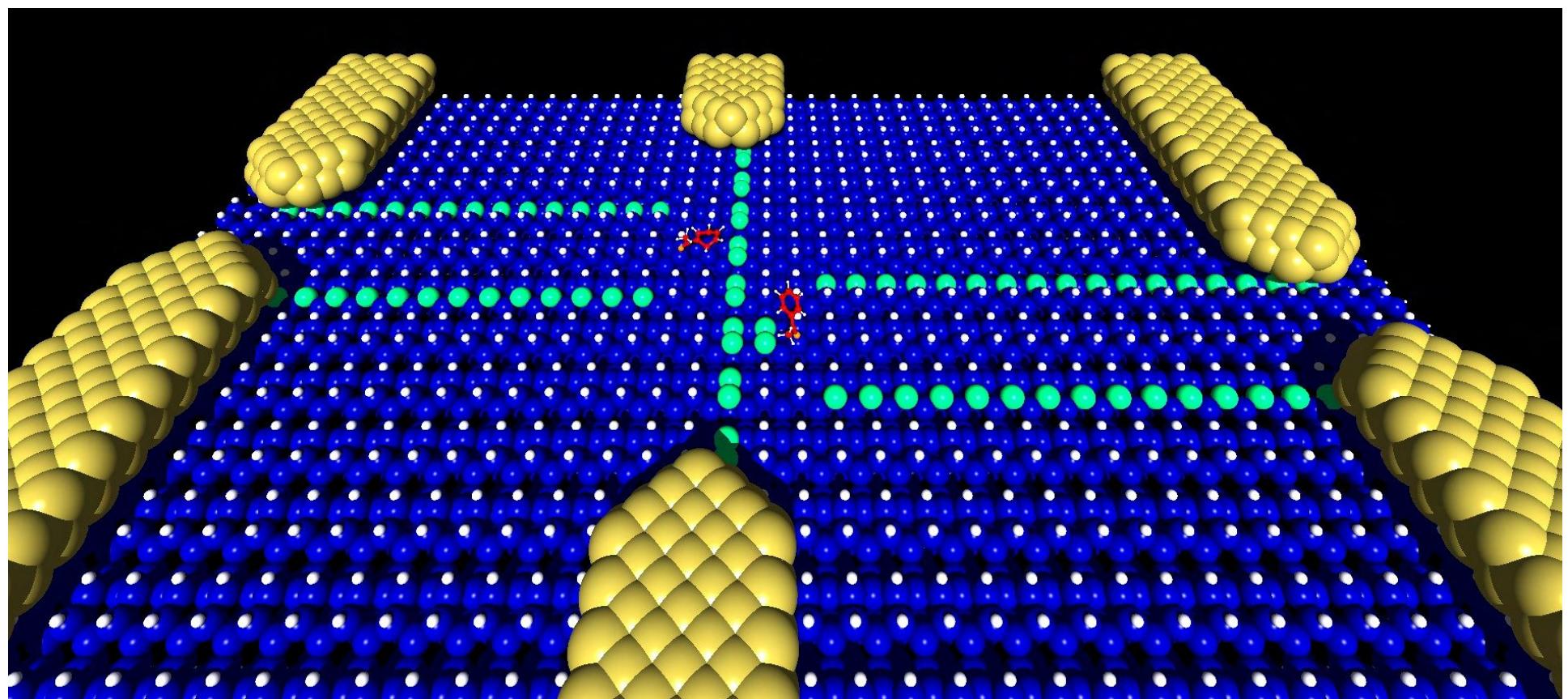




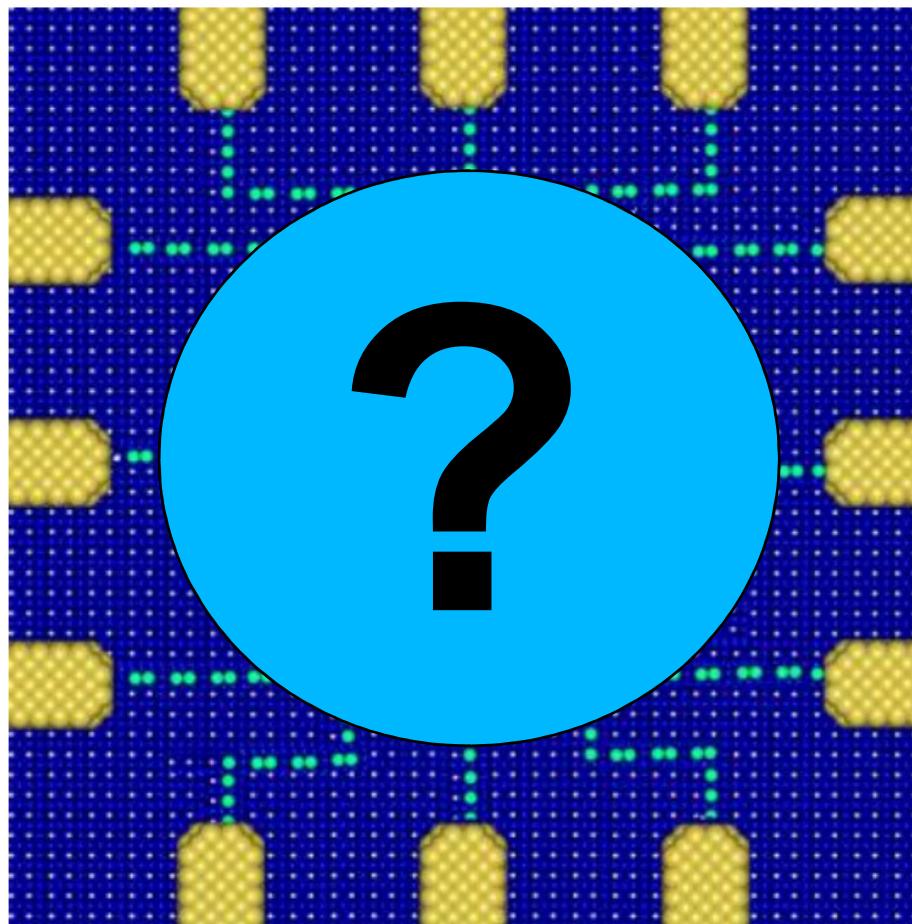








-QHC (Quantum Hamiltonian Computing) circuits



THANK YOU